

**6U VPX Power & GND
1 Slot BACKPLANE
J1-J6 Universal Pattern**

**VITA 46
VITA 65**

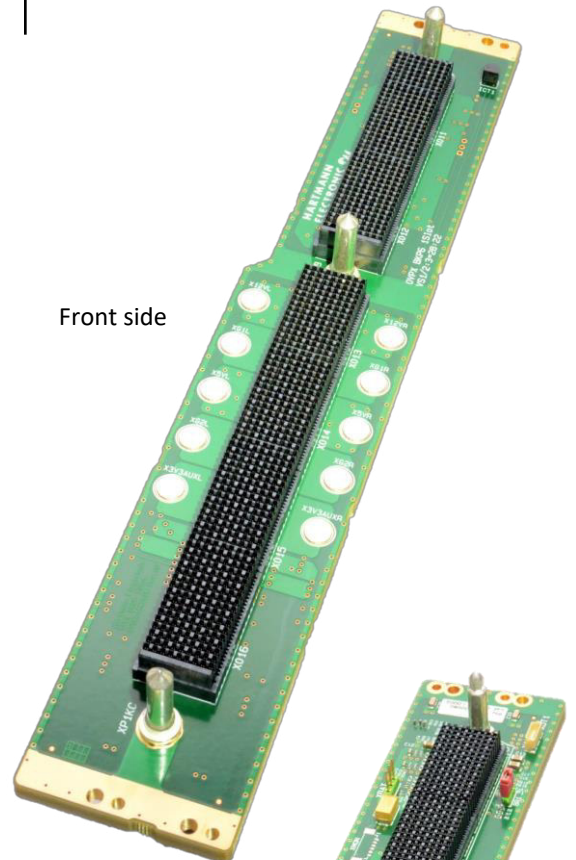


Key Features:

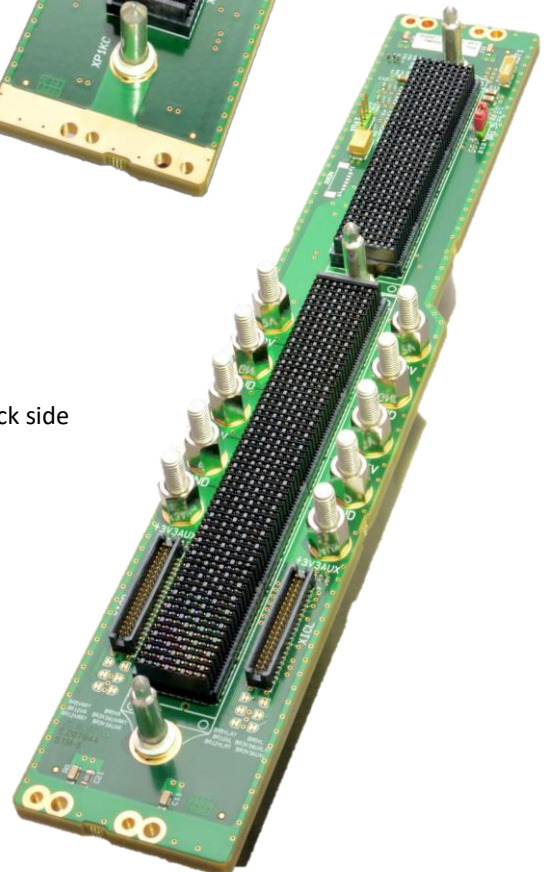
- **Our VPX Power & Ground Backplanes - the development tool for VPX systems of your first choice**
- Compliant to VITA 46.0 baseline specification
- Compliant to VITA 65
- 1 Slot VPX with Universal Pattern J1-J6
- with RTM for all slots and pins
- M5 studs for power entry
- PCB size 261.85 mm x 47.46 mm x 5.4 mm
- Flexible keying and alignment mechanism
- with geographical address pins
- Reference clock
- Auxiliary clock
- System Reset
- With JTAG connector
- System Management Interface on the backplane (I2CA, I2CB)
- Non-Volatile Memory Read Only signal set by Jumper BR1
- Battery backup option setting by Jumper XBAT. Vbat external or connected to 3.3 VAUX.
- Max. Input current per backplane
VS1/VS2:VS3 = 60A : 60A
- Operating temperature: -40°C - +85°C
- Storage temperature: -55°C - +85°C
- Flammability rating: UL94-V0
- Custom assembly or modification on request

▪ **Order number: B196100010**

Front side



Back side



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2) Pin Assignment

Pin Assignment VPX J0 (Utility Connector)

	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Vs1	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2	Vs2
2	Vs1	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2	Vs2
3	Vs3	Vs3	Vs3	Vs3	No Pad*	Vs3	Vs3	Vs3	Vs3
4	GND	SM2	SM3	GND	-12V_Aux	GND	SYSRESET*	NVMRO	GND
5	GND	GAP*	GA4*	GND	3.3V_Aux	GND	SM0	SM1	GND
6	GND	GA3*	GA2*	GND	+12V_Aux	GND	GA1*	GA0*	GND
7	TCK	GND	GND	TDO	TDI	GND	GND	TMS	TRST*
8	GND	REF_CLK-	REF_CLK+	GND	GND	AUX_CLK-	AUX_CLK+	GND	GND

Vs1/Vs2=12V, Vs3=5V

Pin Assignment J1– J6

This connector is all User Defined pins. See Section VITA 65 6.3.3 for requirements and pin assignments concerning connectors that are all User Defined.

Backplane Jn	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	UD	UD	UD	UD	GND	UD	UD	UD
2	GND	UD	UD	GND	UD	UD	UD	UD	GND
3	UD	UD	UD	UD	UD	GND	UD	UD	UD
4	GND	UD	UD	GND	UD	UD	UD	UD	GND
5	UD	UD	UD	UD	UD	GND	UD	UD	UD
6	GND	UD	UD	GND	UD	UD	UD	UD	GND
7	UD	UD	UD	UD	UD	GND	UD	UD	UD
8	GND	UD	UD	GND	UD	UD	UD	UD	GND
9	UD	UD	UD	UD	UD	GND	UD	UD	UD
10	GND	UD	UD	GND	UD	UD	UD	UD	GND
11	UD	UD	UD	UD	UD	GND	UD	UD	UD
12	GND	UD	UD	GND	UD	UD	UD	UD	GND
13	UD	UD	UD	UD	UD	GND	UD	UD	UD
14	GND	UD	UD	GND	UD	UD	UD	UD	GND
15	UD	UD	UD	UD	UD	GND	UD	UD	UD
16	GND	UD	UD	GND	UD	UD	UD	UD	GND

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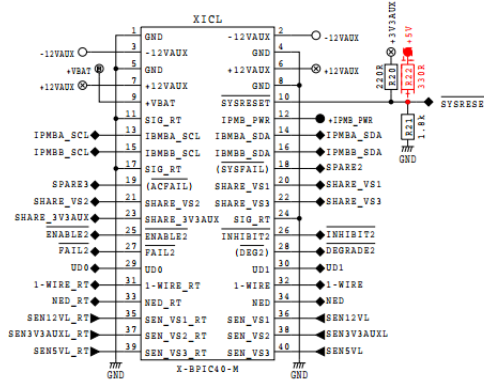
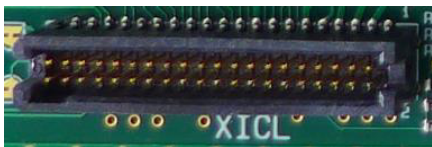
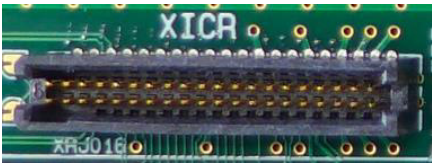
**VITA 46
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3) Current Capability:

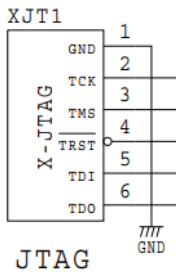
- +12V 60 A
- +12V 60 A
- +5V 60 A
- +3.3V AUX 10 A

4) UTILITY/SHARE/SENSE (Multi-Connector XICL/ XICR)



Connector Samtec
TFM-120-02-L-D-P

5) JTAG (Connector XJT1)



Connector J.S.T.
BM06B-SRSS-TB(LF)(SN)

6) SYSCON

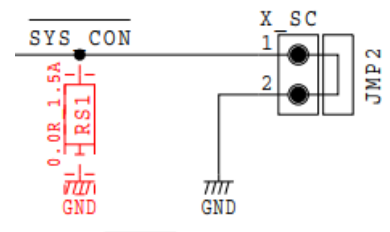
By setting the signal Syscon to GND the system slot is defined. In general the system slot is slot 1.

We offer 2 options for setting:

- Jumper (standard)
- 0 Ohm Resistor for rugged applications

X_SC

1	SYSCON1
2	GND
3	SYSCON2



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7) Power Connections M3 screws

The main operating voltages and GND are supplied with M5 screw.

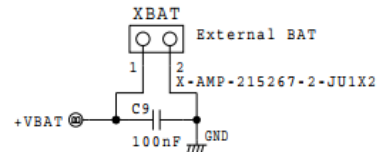


8) XBAT

Normally a battery voltage with approximately 3V is available at Pin VBAT of connector VPX-J1. The voltage is externally accessible with connector XBAT, Pin2 or internally using 3.3V_AUX by setting a Jumper between Pin2 and Pin3.

VBAT X5

1	GND
2	+VBAT
3	+3.3V_AUX

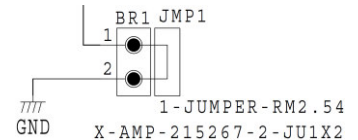


9) NVMRO

If Jumper BR1 is closed NVRMO is set to memory writeable.

BR1

1	NVMRO
2	GND



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