

1 2 3

A B C D

TOP VIEW

X1

1 P2

1 P1

Made in Germany

Coding Key

V(I/O)	
+3.3V	+5V
Cadmium Yellow	Brilliant Blue

Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7

Rev.BA

Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7

Rev.BA

Rev Description Date Name

2009 31.07. Wiedmann

Check 31.07. Wiedmann

DIN A4

M 1:1

Number

Title

Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7

Rev.BA

Device: HE-C33RD014--4 File: BP7

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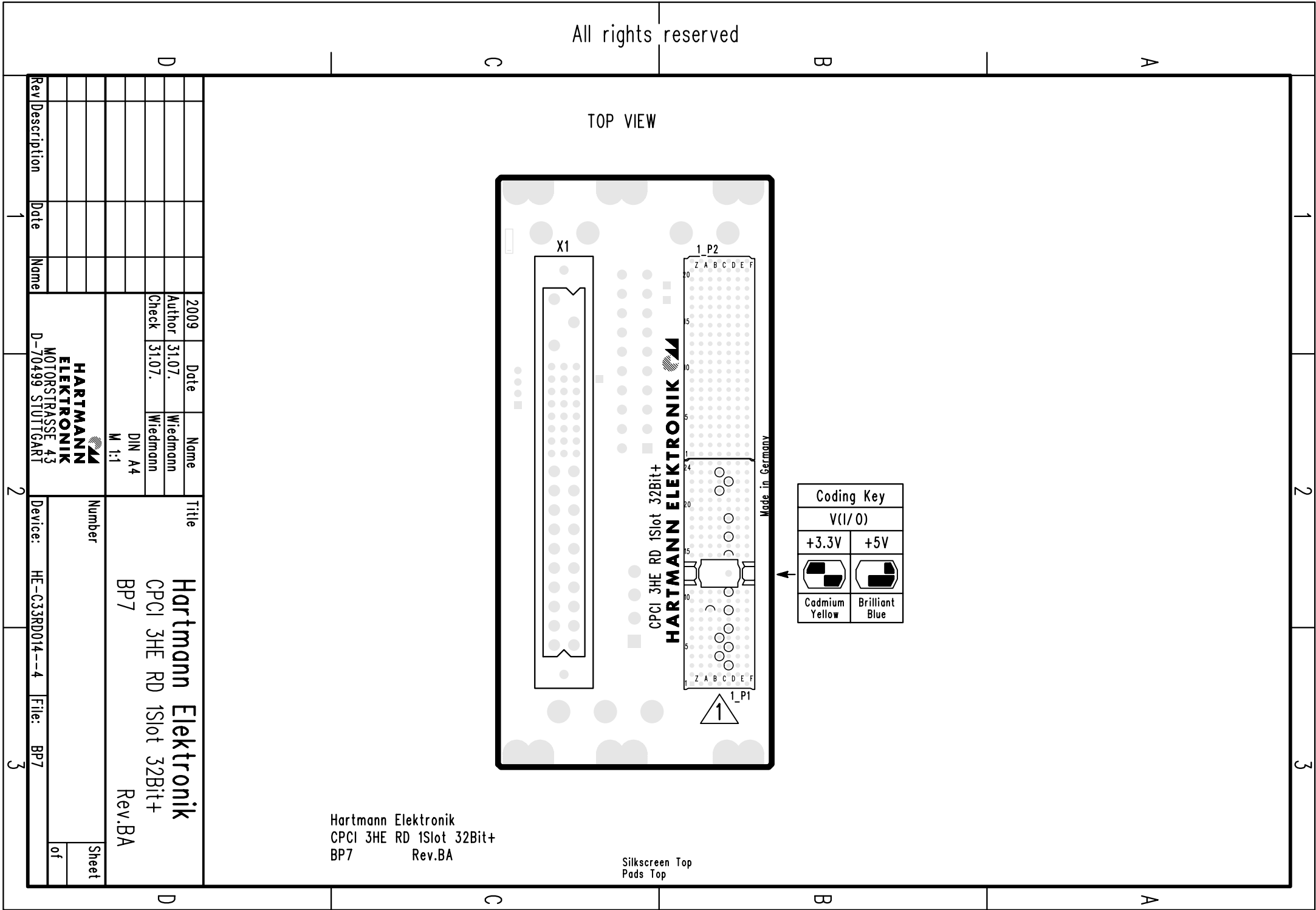
1

2

3

A B C D

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[illegible]

1 2 3

A B C D

TOP VIEW

X1

1 P2

1 P1

Made in Germany

Coding Key

V(I/O)	
+3.3V	+5V
Cadmium Yellow	Brilliant Blue

Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7

Rev.BA

Hartmann Elektronik
MOTORSTRASSE 43
D-70499 STUTTGART

2009
31.07.
31.07.

Author
Check

Wiedmann
Wiedmann

DIN A4
M 1:1

Title
Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7

Number

Device: HE-C33RD014--4

File: BP7

Sheet
of

Rev Description Date Name

1

2

3

A B C D

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Silkscreen Top
Pads Top

1 2 3

A B C D

TOP VIEW

X1

1 P2

1 P1

Made in Germany

Coding Key

V(I/O)	
+3.3V	+5V
Cadmium Yellow	Brilliant Blue

Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7

Rev.BA

Hartmann Elektronik
MOTORSTRASSE 43
D-70499 STUTTGART

2009
31.07.
31.07.

Name
Wiedmann
Wiedmann
DIN A4
M 1:1

Title
Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7
Rev.BA

Number

Device: HE-C33RD014--4

File: BP7

Sheet
of

Rev Description Date Name

1

2

3

A B C D

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Silkscreen Top
Pads Top

1 2 3

A B C D

TOP VIEW

X1

1 P2

1 P1

Made in Germany

Coding Key

V(I/O)	
+3.3V	+5V
Cadmium Yellow	Brilliant Blue

Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7

Rev.BA

Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7

Rev.BA

Rev Description Date Name

2009 31.07. Wiedmann

Check 31.07. Wiedmann

DIN A4

M 1:1

Number

Title

Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7

Rev.BA

Device: HE-C33RD014--4 File: BP7

Sheet of

All rights reserved

Silkscreen Top
Pads Top

All rights reserved

Without geographic addressing on request

BOTTOM VIEW

Shrouds 1P1
on request

Busbar for +5V keying

Busbar for +3.3V keying

Silkscreen Bottom
Pads Bottom

Rev	Description	Date	Name
1			
2			
3			

Author	Date	Name
Wiedmann	31.07.	
Check	31.07.	

Title	Number
Hartmann Elektronik	
CPCl 3HE RD 1Slot 32Bit+	
BP7	
Rev.BA	

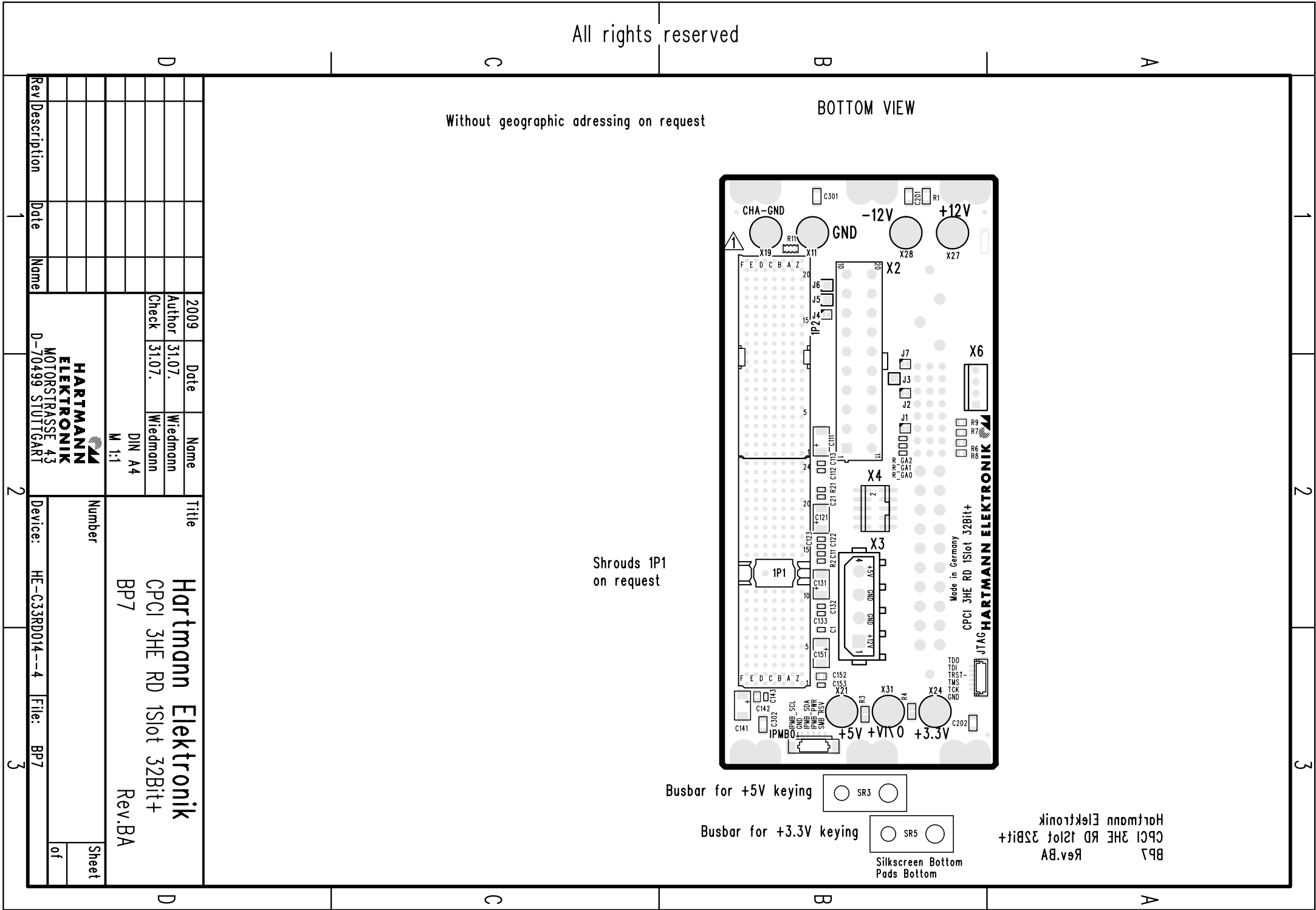
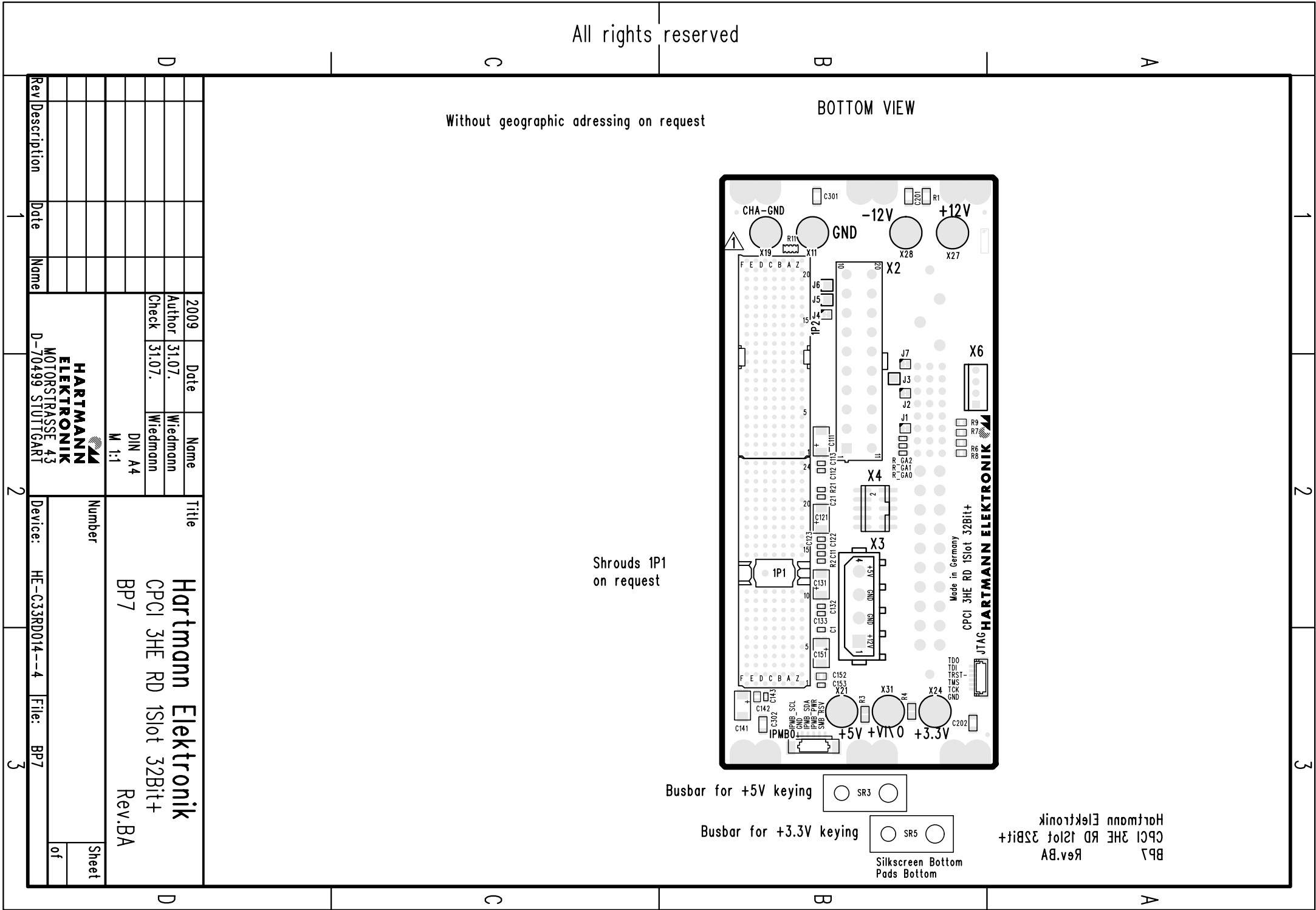
Device:	File:
HE-C33RD014--4	BP7

Sheet	of
1	1

HARTMANN ELEKTRONIK
D-70499 STUTTGART

Made in Germany
CPCl 3HE RD 1Slot 32Bit+
JTAG

Rev.BA
BP7
CPCl 3HE RD 1slot 32Bit+
Hartmann Elektronik

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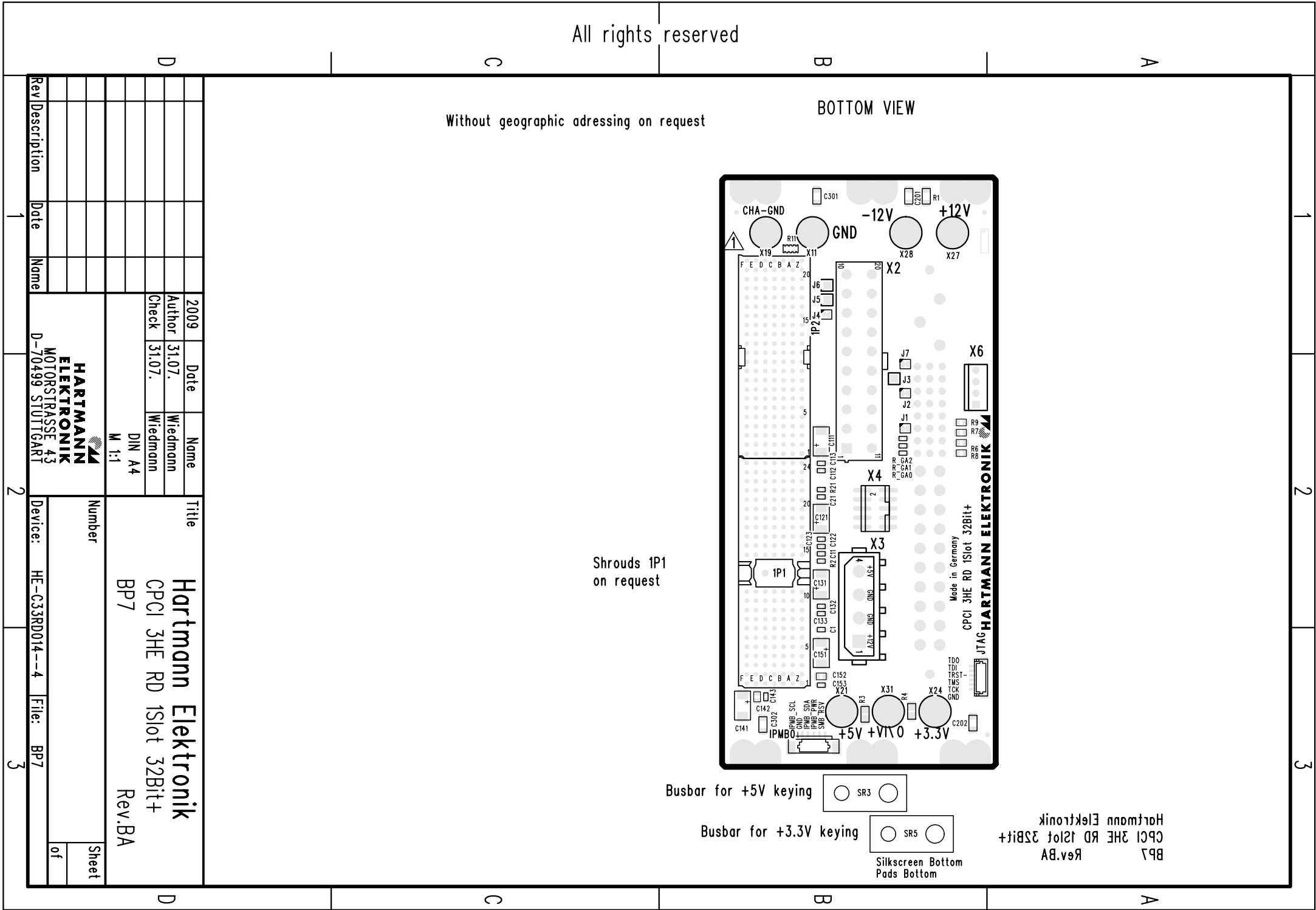
A
B
C
D

1
2
3

Without geographic addressing on request

Shrouds 1P1 on request

BOTTOM VIEW



A
B
C
D

1
2
3

Without geographic addressing on request

Shrouds 1P1 on request

BOTTOM VIEW

A
B
C
D

1
2
3

Without geographic addressing on request

Shrouds 1P1 on request

BOTTOM VIEW

All rights reserved

Without geographic addressing on request

BOTTOM VIEW

Shrouds 1P1
on request

Busbar for +5V keying

Busbar for +3.3V keying

Silkscreen Bottom
Pads Bottom

Rev	Description	Date	Name
1			
2			
3			

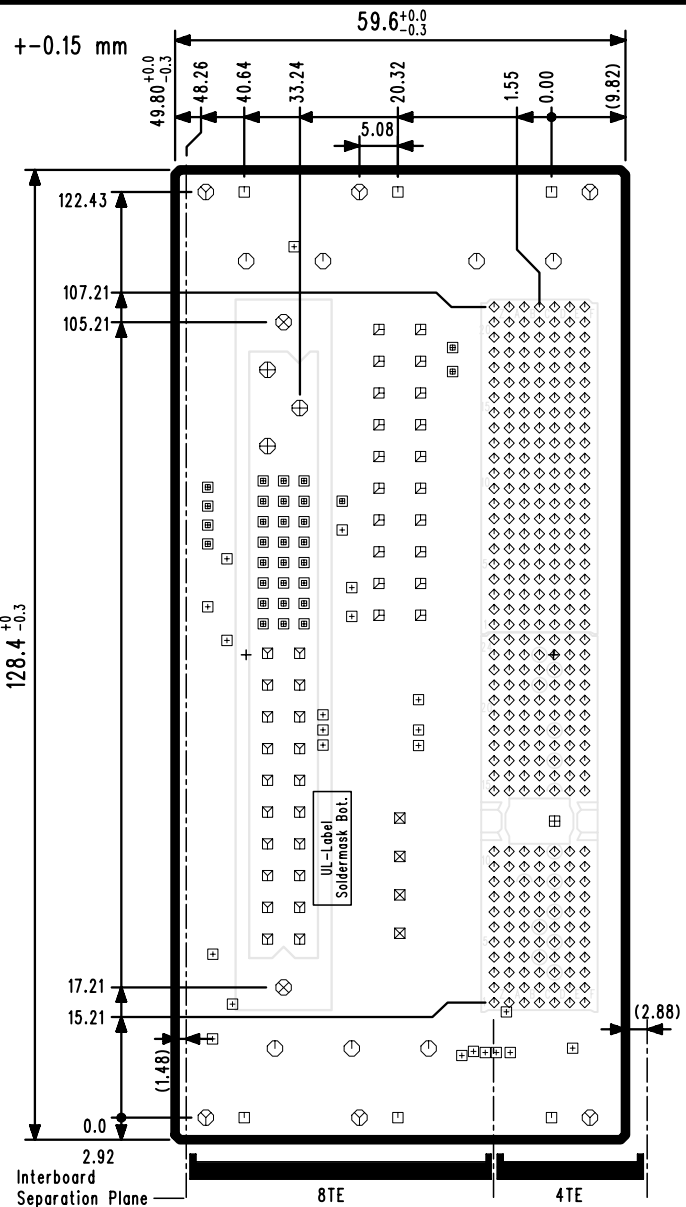
Author	Date	Name
Wiedmann	31.07.	
Check	31.07.	

Title	Number
Hartmann Elektronik	
CPCl 3HE RD 1Slot 32Bit+	
BP7	
Rev.BA	

Device:	File:
HE-C33RD014--4	BP7

Sheet	of
1	1

Hartmann Elektronik
CPCl 3HE RD 1Slot 32Bit+
Rev.BA
BP7



Hartmann Elektronik
CPCI 3HE RD 1Slot 32Bit+
BP7 Rev.BA

Title			2009		
Hartmann Elektronik			Date	Name	
CPCI 3HE RD 1Slot 32Bit+ BP7 Rev.BA			31.07.	Wiedmann	
			31.07.	Wiedmann	
			DIN A4 M 1:1		
Number					
HE-C33RD014--4 BP7			 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART		
Device:			Name		
HE-C33RD014--4			D-70499 STUTTGART		
File:					
BP7					
Sheet					
of					

Silkscreen Top
Pads Top

1 2 3

A B C D

TOP VIEW

X1

2_P2

1_P2

2_P1

1

2

Made in Germany

Coding Key

V(I/O)	
+3.3V	+5V
Cadmium Yellow	Brilliant Blue

Hartmann Elektronik
CPCI 3HE RD 2Slot 32Bit+
BP6

Rev.BA

Hartmann Elektronik
CPCI 3HE RD 2Slot 32Bit+
BP6

Rev.BA

Rev Description Date Name

2009 Date Name

Author Wiedmann

Check 28.07. Wiedmann

DIN A4

M 1:1

Number

Title

Hartmann Elektronik

CPCI 3HE RD 2Slot 32Bit+ BP6

Rev.BA

Device: HE-C33RD024--4 File: BP6

Sheet of

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A

B

C

D

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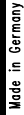
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↑

Silkscreen Top
Pads Top

Sheet
of

3



of

A

1 2 3

A B C D

Common Tolerance: $\pm 0.15 \text{ mm}$

100.2 $^{+0.0}_{-0.3}$

49.80 $^{+0.0}_{-0.3}$

48.26

40.64

33.24

20.32

1.55

0.00

4 x 20.32 = 81.28

5.08

(9.82)

122.43

107.21

105.21

128.4 $^{+0}_{-0.3}$

17.21

15.21

0.0

2.92

Interboard Separation Plane

8TE

4TE

4TE

4TE

(2.88)

(1.48)

UL-label
Soldermask Bot.

Hartmann Elektronik
CPCI 3HE RD 3Slot 32Bit+
BUS845 Rev.BA

2004
10.09.
10.09.
DIN A4
M 1:1

Hartmann Elektronik
MOTORSTRASSE 43
D-70499 STUTTGART

RevDescription Date Name

1

2

3

1

2

3

A

B

C

D

Sheet
of

Rev.BA

BUS845

CPCI 3HE RD 3Slot 32Bit+

Hartmann Elektronik

1 2 3

A B C D

Common Tolerance: $\pm 0.15 \text{ mm}$

100.2 $^{+0.0}_{-0.3}$

49.80 $^{+0.0}_{-0.3}$

48.26

40.64

33.24

20.32

1.55

0.00

4 x 20.32 = 81.28

5.08

(9.82)

122.43

107.21

105.21

128.4 $^{+0}_{-0.3}$

17.21

15.21

0.0

2.92

Interboard Separation Plane

8TE

4TE

4TE

4TE

(2.88)

(1.48)

UL-Label
Soldermask Bot.

Hartmann Elektronik
CPCI 3HE RD 3Slot 32Bit+
BUS845 Rev.BA

2004
10.09.
10.09.
DIN A4
M 1:1

Hartmann Elektronik
MOTORSTRASSE 43
D-70499 STUTTGART

RevDescription Date Name

1

2

3

1

2

3

A

B

C

D

Sheet
of

Number

Title

Hartmann Elektronik
CPCI 3HE RD 3Slot 32Bit+
BUS845 Rev.BA

Device: HE-C33RD034--4 File: BUS845BA

Silkscreen Top
Pads Top

[illegible]

BOTTOM VIEW

JTAG
Pin Assignment

Pin	Signal
1	GND
2	TCK
3	TMS
4	TRST-
5	TDI
6	TDO



Busbar for +3.3V keying



Sheet	
of	

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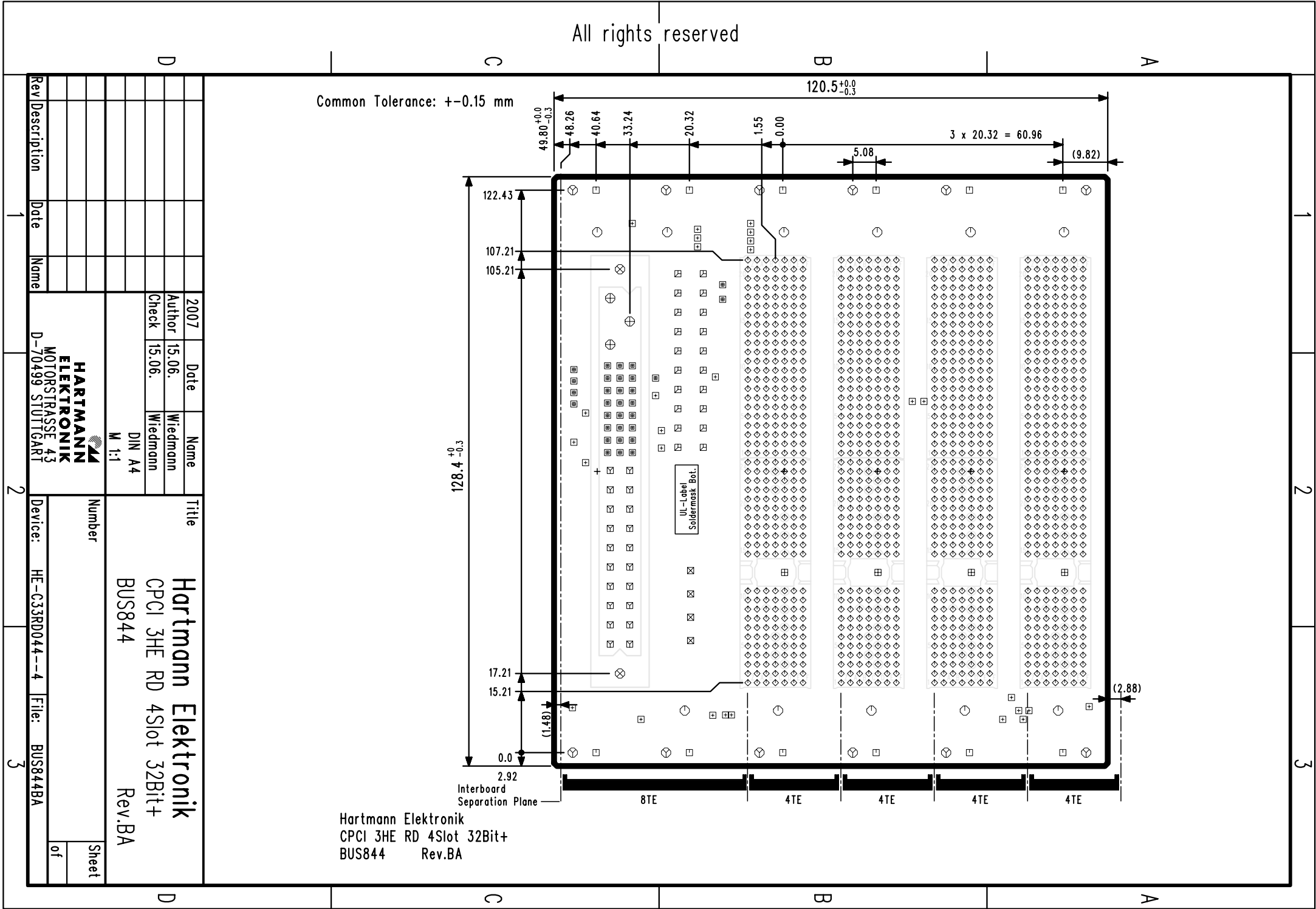
The drawing is a technical representation of a printed circuit board (PCB) layout. It features a central rectangular area representing the board, surrounded by a border. The board is populated with various components, including a large central component labeled 'UL-Label Soldermask Bot.' and several smaller components. Dimensions are provided for the board size and component placement. The title block at the bottom contains the following information:

Rev	Description	Date	Name	2007	Date	Name	Title
				Author	15.06.	Wiedmann	Hartmann Elektronik CPCI 3HE RD 4Slot 32Bit+ BUS844 Rev.BA
				Check	15.06.	Wiedmann	

Additional information in the title block includes: Hartmann Elektronik, MOTORSTRASSE 43, D-70499 STUTTGART, DIN A4, M 1:1, Number, Device: HE-C33RD044--4, File: BUS844BA, Sheet of.

Dimensions and labels on the drawing include:

- Common Tolerance: ± 0.15 mm
- Board dimensions: $120.5^{+0.0}_{-0.3}$ (width), $128.4^{+0.3}_{-0.3}$ (height)
- Component footprints: 8TE, 4TE, 4TE, 4TE, 4TE
- Labels: UL-Label Soldermask Bot., Interboard Separation Plane
- Dimensions: 49.80, 48.26, 40.64, 33.24, 20.32, 1.55, 0.00, 5.08, 3 x 20.32 = 60.96, (9.82), 122.43, 107.21, 105.21, 17.21, 15.21, 0.0, 2.92, (48.1), (2.88)



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The drawing is a technical representation of a printed circuit board (PCB) layout. It includes a title block with the following information:

- Rev Description:** 1
- Date:** 2007
- Name:** Wiedmann
- Author:** 15.06.
- Check:** 15.06.
- DIN A4**
- M 1:1**
- Number:** BUS844
- Rev.BA**
- Device:** HE-C33RD044--4
- File:** BUS844BA
- Sheet:** 1 of 3

The PCB layout itself is rectangular with a width of 120.5^{+0.0}_{-0.3} mm and a height of 128.4^{+0.3}_{-0.3} mm. It features a central area with a grid of components, including a large section labeled "UL-Label Soldermask Bot." and a section labeled "Interboard Separation Plane". Dimensions are provided for various sections and components, such as 49.80^{+0.0}_{-0.3}, 48.26, 40.64, 33.24, 20.32, 1.55, 0.00, 5.08, 3 x 20.32 = 60.96, (9.82), 122.43, 107.21, 105.21, 17.21, 15.21, 0.0, 2.92, and (2.88). The layout is divided into sections labeled 8TE, 4TE, 4TE, 4TE, and 4TE. The drawing is oriented with A at the top and B at the bottom.

All rights reserved

The drawing is a technical representation of a printed circuit board (PCB) layout. It includes a title block with the following information:

- Rev Description:** 1
- Date:** 2007
- Name:** Wiedmann
- Author:** 15.06.
- Check:** 15.06.
- DIN A4**
- M 1:1**
- Number:** BUS844
- Rev.BA**
- Device:** HE-C33RD044--4
- File:** BUS844BA
- Sheet:** 1 of 3

The PCB layout itself is rectangular with a width of 120.5^{+0.0}_{-0.3} mm and a height of 128.4^{+0.3}_{-0.3} mm. It features a central area with a grid of components, including a large section labeled "UL-Label Soldermask Bot." and a section labeled "Interboard Separation Plane". Dimensions are provided for various sections and components, such as 49.80^{+0.0}_{-0.3}, 48.26, 40.64, 33.24, 20.32, 1.55, 0.00, 5.08, 3 x 20.32 = 60.96, (9.82), 122.43, 107.21, 105.21, 17.21, 15.21, 0.0, 2.92, and (2.88). The layout is divided into sections labeled 8TE, 4TE, 4TE, 4TE, and 4TE. The drawing is oriented with A at the top and B at the bottom.

The diagram illustrates the layout of a 32-bit CPCI slot. It includes a side view labeled 'X1' showing the slot's profile. The main layout shows five pin headers, numbered 1 to 5, each with a 24-pin configuration. The headers are labeled as follows:

- Header 1: 1_P2 (pins 1-24) and 1_P1 (pins 25-48)
- Header 2: 2_P2 (pins 1-24) and 2_P1 (pins 25-48)
- Header 3: 3_P2 (pins 1-24) and 3_P1 (pins 25-48)
- Header 4: 4_P2 (pins 1-24) and 4_P1 (pins 25-48)
- Header 5: 5_P2 (pins 1-24) and 5_P1 (pins 25-48)

The pin headers are arranged in a row, with the 1_P2 headers on the left and the 1_P1 headers on the right. The diagram also shows the internal wiring and the location of the 32-bit data bus pins. The text 'HARTMANN ELEKTRONIK' is visible in the center, and 'CPCI 3HE RD 5Slot 32Bit+' is written vertically on the right side. The text 'Made in Germany' is also present.

Coding Key	
V(I/O)	
+3.3V	+5V
	
Cadmium Yellow	Brilliant Blue

Silkscreen Top
Pads Top

[illegible]

[illegible][illegible]

X1

HARTMANN ELEKTRONIK

PCI 3HE RD 6Slot 32Bit+

Made in Germany

6 P2

5 P2

4 P2

3 P2

2 P2

1 P2

6 P1

5 P1

4 P1

3 P1

2 P1

1 P1

①

②

③

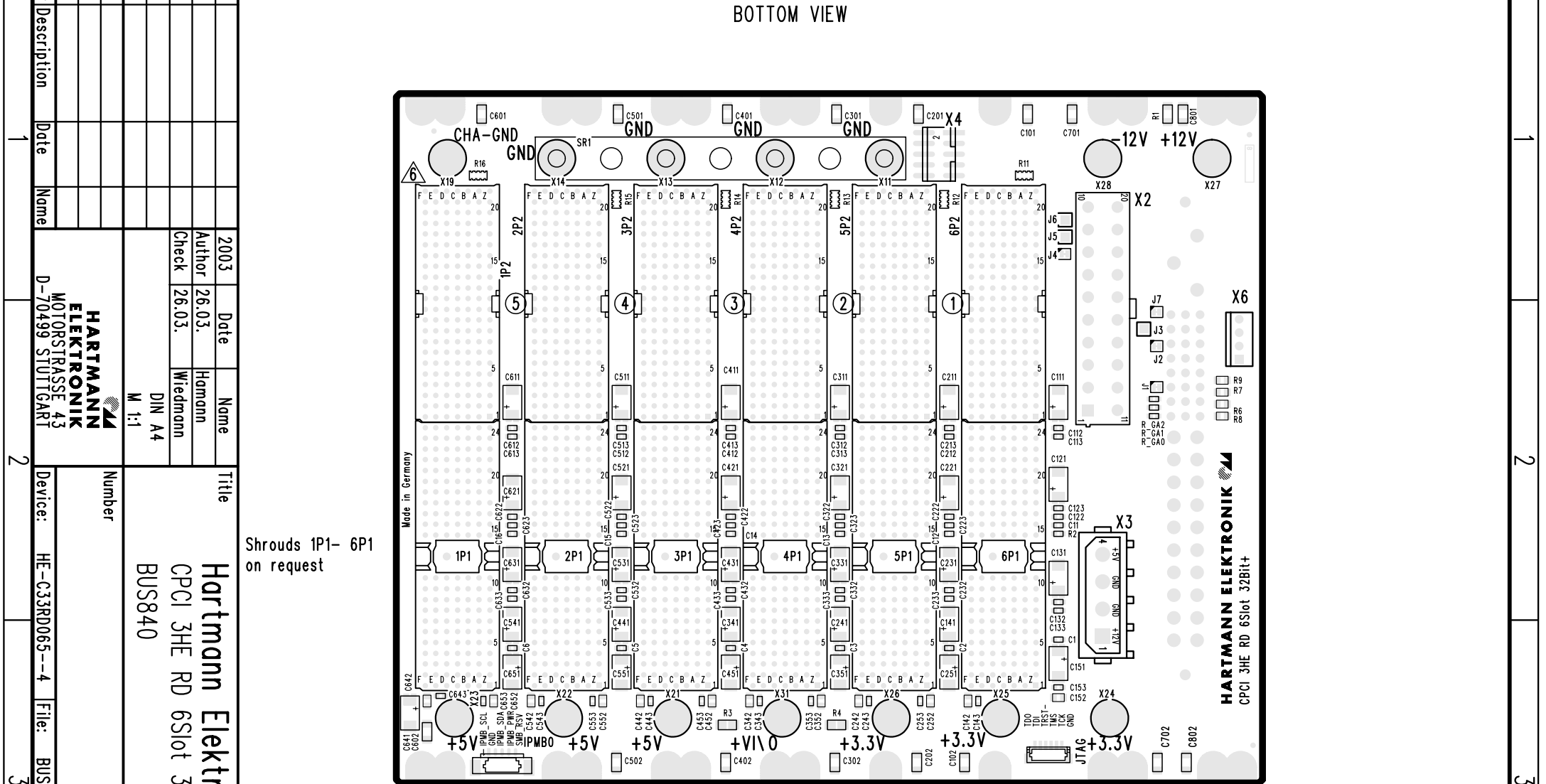
④

⑤

⑥

↑

Silkscreen Top
Pads Top

[illegible]

☐ SR3 ☐	☐	☐	☐
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☐ SR5 ☐ ☐ ☐ ☐

☐ SR4 ☐	☐	☐	☐	☐	☐
---	-----------------------	-----------------------	-----------------------	-----------------------	-----------------------

Busbar for +5V keying

Busbar for +3.3V keying

SR2 SR3 SR4 SR5

Silkscreen Bottom Pads Bottom

A	B	C	D
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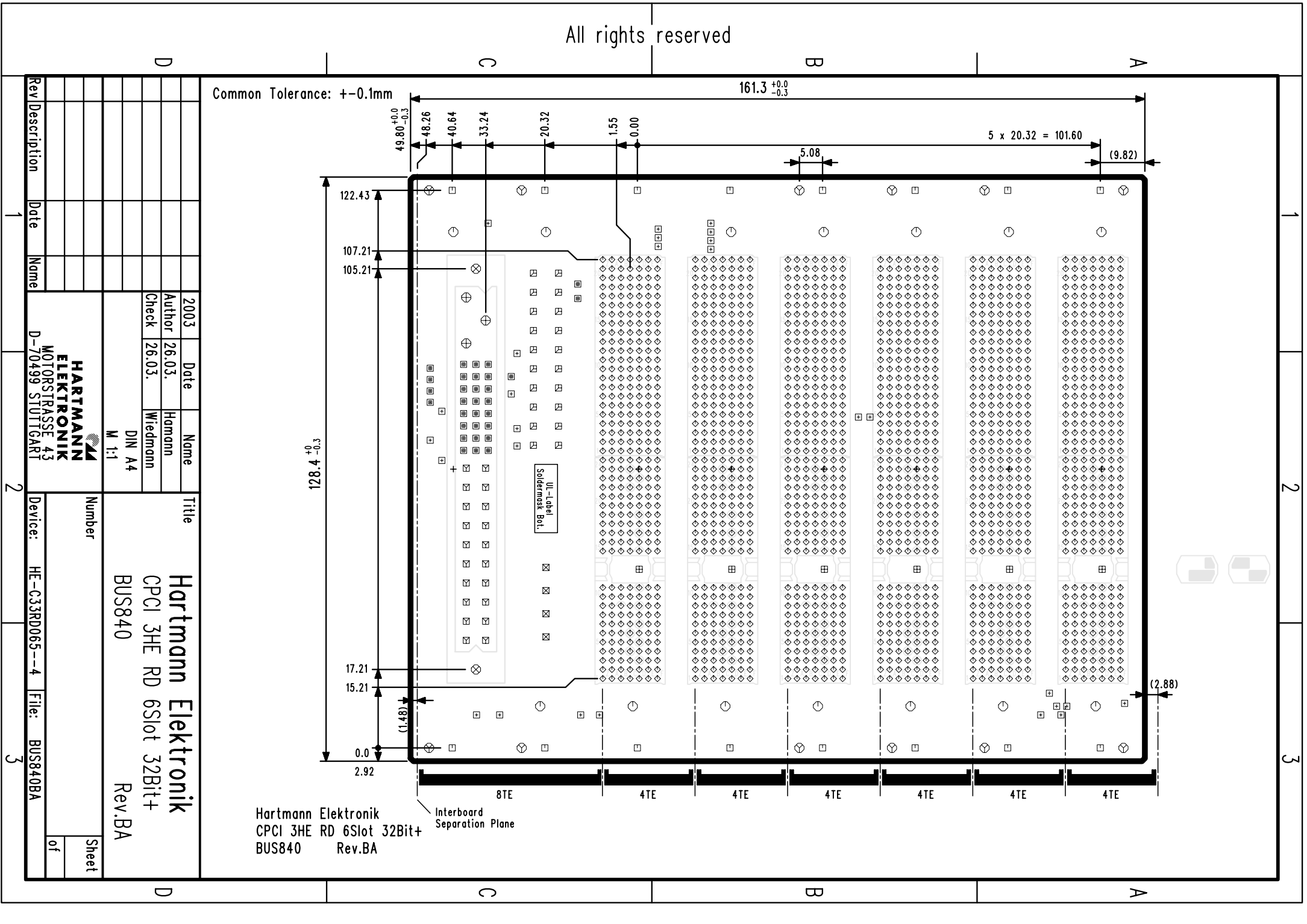
[illegible][illegible][illegible][illegible]

Figure 1 shows the dimensions and pin connections of the 3HE RD 7Slot 32Bit+ card. The side view shows a card with a width of 100mm and a height of 100mm. The top view shows the card with 7 slots, each with 24 pins. The pins are labeled 1 to 24, and the slots are labeled 1 to 7. The card is labeled 'HARTMANN ELEKTRONIK' and 'CPCI 3HE RD 7Slot 32Bit+'. The card is made in Germany.

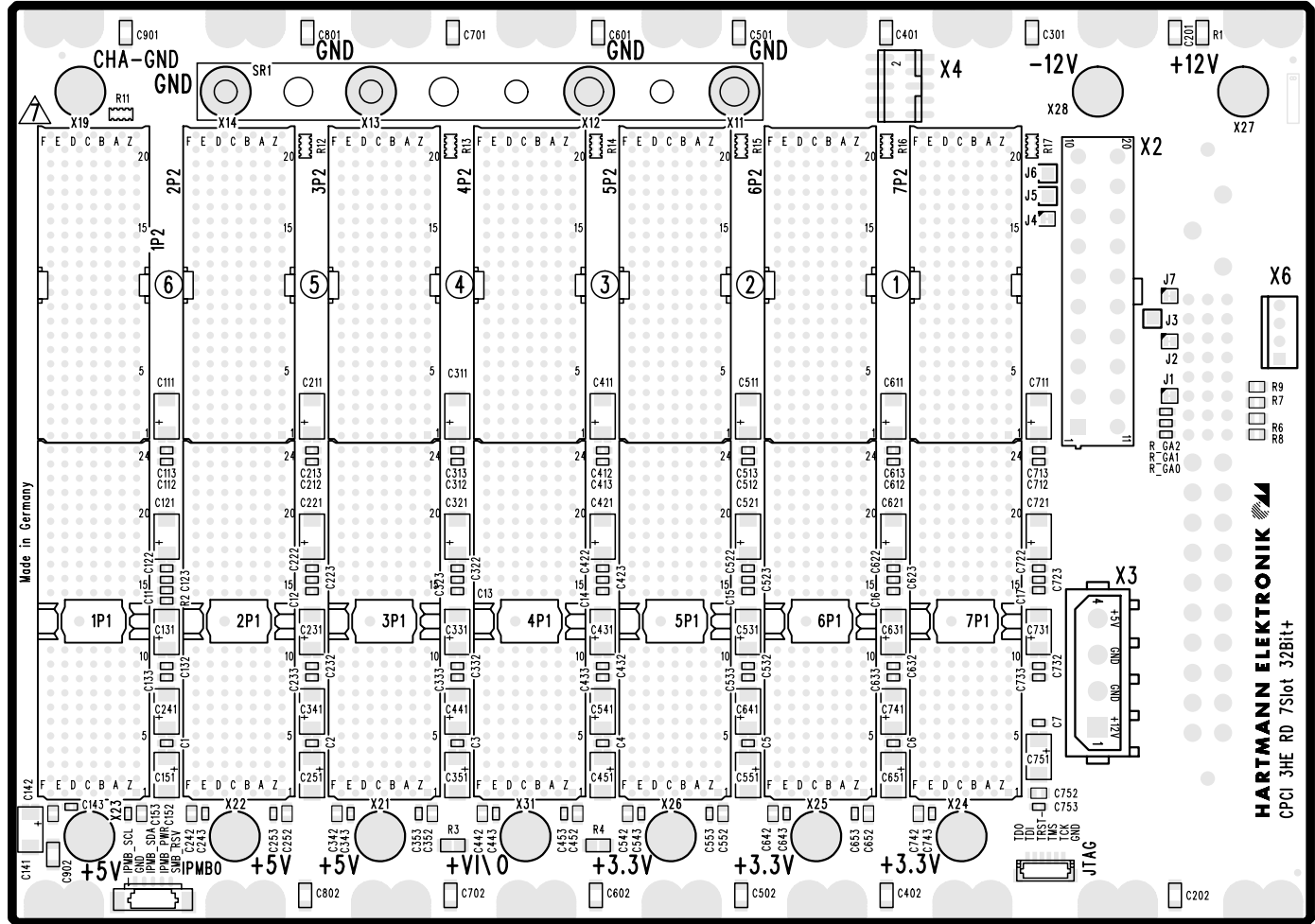
Coding Key	
V(I/O)	
+3.3V	+5V
	
Cadmium Yellow	Brilliant Blue

Silkscreen Top
Pads Top

[illegible]

BOTTOM VIEW

Without geographic addressing on request



Shrouds 1P1- 6P1
on request

Busbar for +5V keying

Busbar for +3.3V keying

Silkscreen Bottom
Pads Bottom

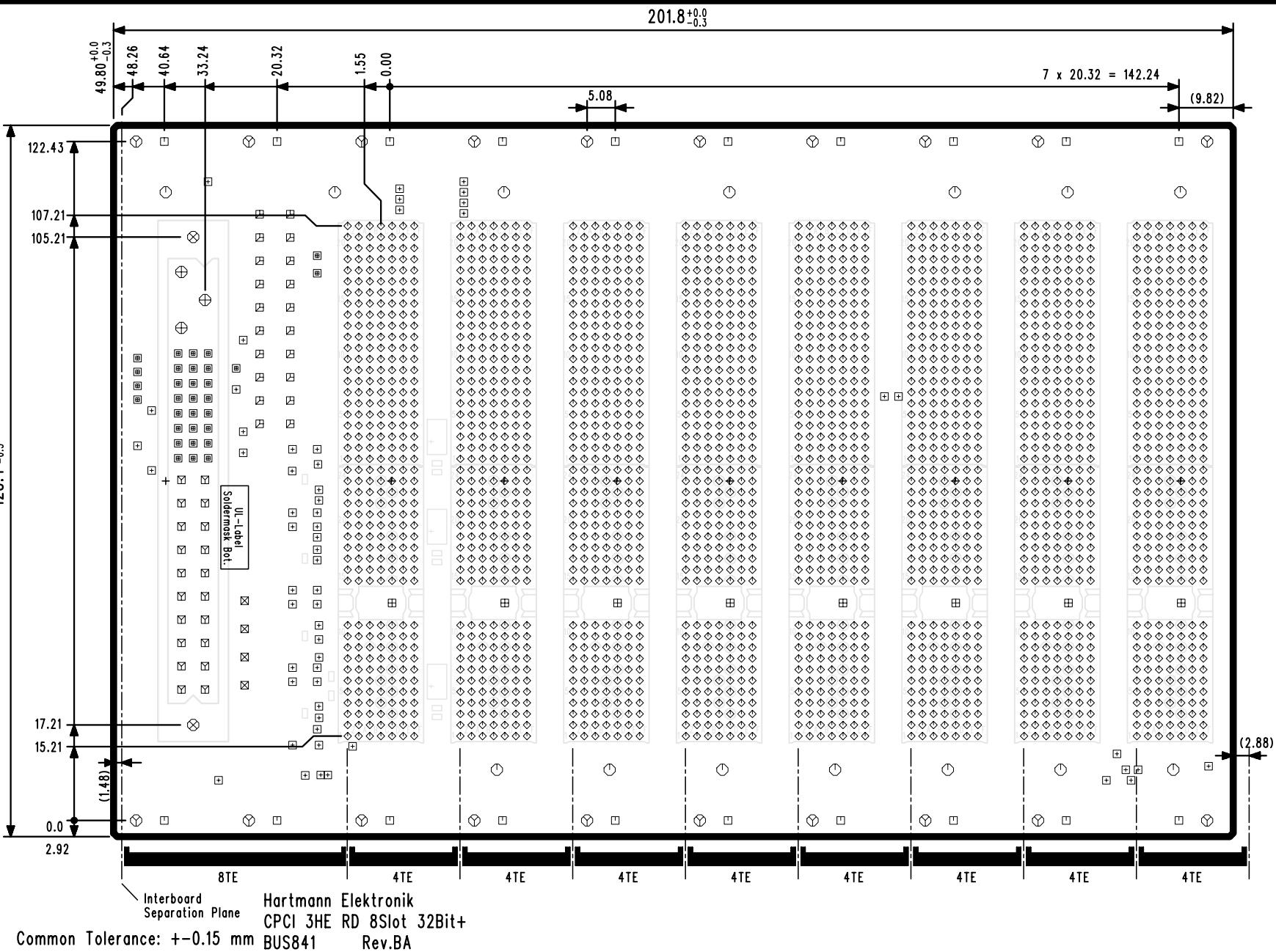
BU2842 Rev.BA
CPCI 3HE RD 7Slot 32Bit+
Hartmann Elektronik

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HARTMANN ELEKTRONIK
MOTORSTRASSE 43
D-70499 STUTTGART

Hartmann Elektronik
CPCI 3HE RD 7Slot 32Bit+
BUS842
Rev.BA

[illegible]



Title			Hartmann Elektronik CPCI 3HE RD 8Slot 32Bit+ BUS841 Rev.BA				
2003	Date	Name					
Author	29.10.	Wiedmann					
Check	29.10.	Hamann					
			DIN A4				
			M 1:1				
			 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART				
			Number	Sheet			
			Device:	HE-C33RD084--4	File:	BUS841BA	of