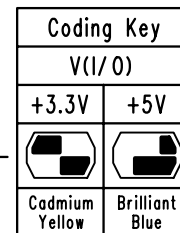


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Silkscreen Top
Pads Top

				2007	Date	Name	Title Hartmann Elektronik CPCI 3HE RA 2Slot 32bit+ BUS676 Rev.BB		
				Author	01.03.	Wiedmann			
				Check	01.03.	Wiedmann			
				DIN A4 M 1:1					
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			Number		Sheet
									of
Rev	Description	Date	Name				Device: HE-C33RA025--4		File: BUS676BB

All rights reserved

Shrouds
1P1 - 2P1
on request

Pin	Signal	Pin	Signal
1	PRST-	2	FAL-
3	DEG-	4	+3.3V-Sense
5	+3.3V	6	GND-Sense (+3.3V)
7	+5V	8	+5V-Sense
9	GND	10	GND-Sense (+5V)

Pin	Signal
1	IPMB_SCL
2	GND
3	IPMB_SDA
4	IPMB_PWR
5	SMB_RSV

Pin	Signal
1	GND
2	TCK
3	TMS
4	TRST-
5	TDI
6	TDO

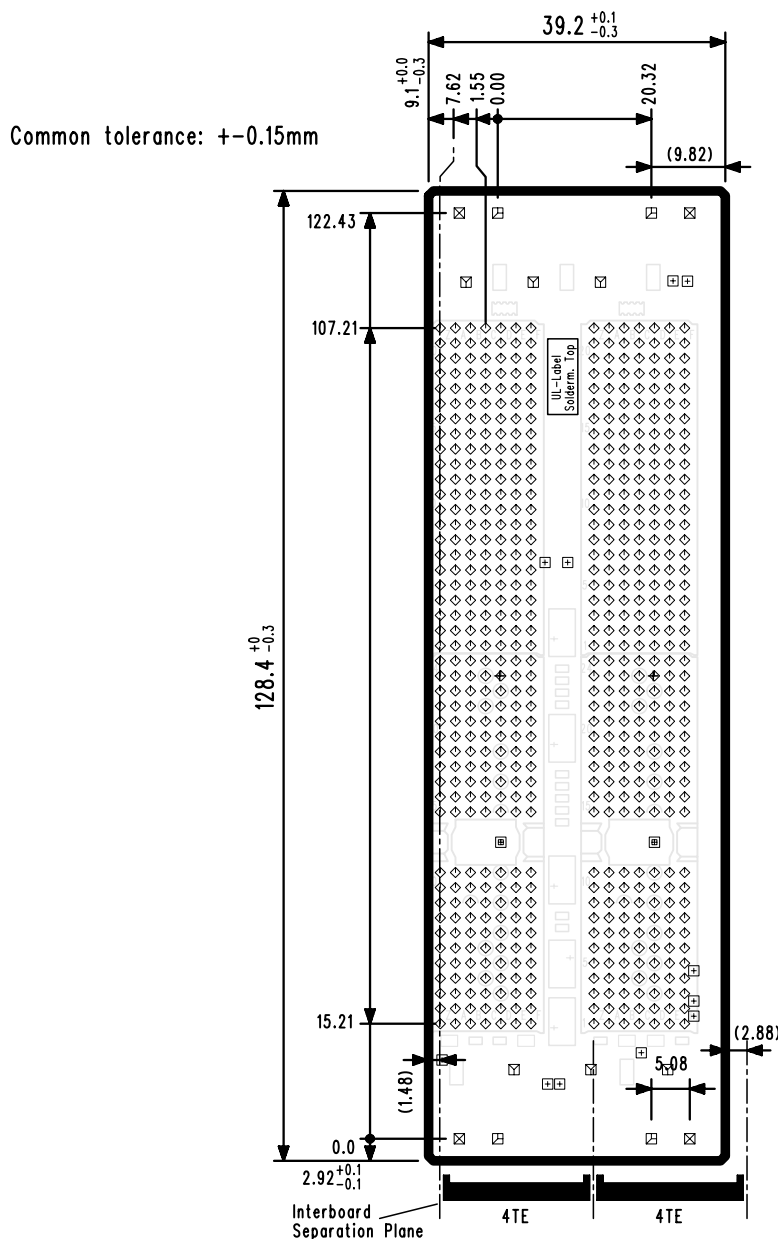
☐ SR2 ☐

○ SR3 ○

BU2676 Rev.BB
CPCI 3HE RA Slot 32bit+
Hartmann Elektronik

Silkscreen Bottom
Pads Bottom

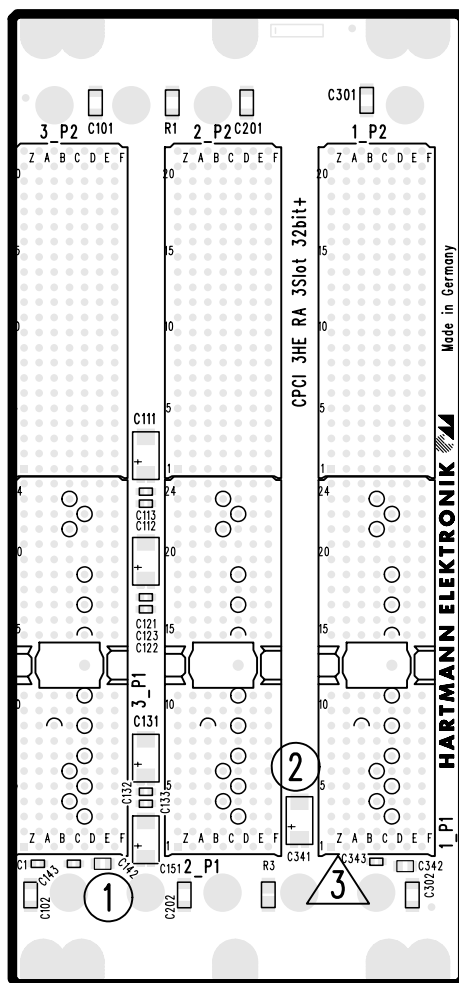
				2007	Date	Name	Title Hartmann Elektronik CPCI 3HE RA 2Slot 32bit+ BUS676 Rev.BB
				Author	01.03.	Wiedmann	
				Check	01.03.	Wiedmann	
				DIN A4 M 1:1			
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			
Rev	Description	Date	Name	Number			Sheet
				Device: HE-C33RA025--4			File: BUS676BB



Hartmann Elektronik
CPCI 3HE RA 2Slot 32bit+
BUS676 Rev.BB

				2007	Date	Name	Title Hartmann Elektronik CPCI 3HE RA 2Slot 32bit+ BUS676 Rev.BB						
				Author	01.03.	Wiedmann							
				Check	01.03.	Wiedmann							
				DIN A4 M 1:1									
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			Number		Sheet				
									of				
Rev	Description	Date	Name							Device:	HE-C33RA025--4	File:	BUS676BB

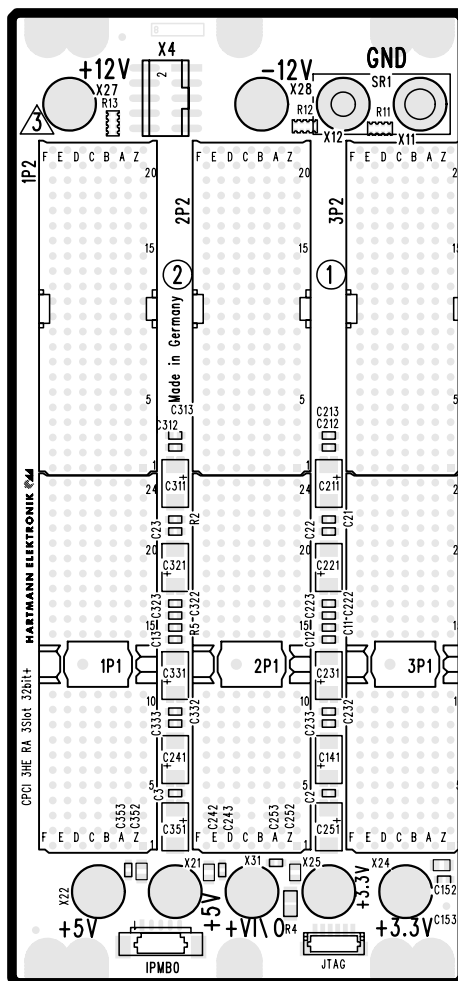
Coding Key	
V(I/O)	
+3.3V	+5V
	
Cadmium Yellow	Brilliant Blue



Silkscreen Top
Pads Top

				2006	Date	Name	Title Hartmann Elektronik CPCI 3HE RA 3Slot 32bit+ BUS675 Rev.BB			
				Author	12.09.	Wiedmann				
				Check	12.09.	Wiedmann				
				DIN A4 M 1:1						
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART						
							Number			Sheet
										of
Rev	Description	Date	Name				Device: HE-C33RA03---4	File: BUS675BB		

Pin	Signal	Pin	Signal
1	PRST-	2	FAL-
3	DEG-	4	+3.3V-Sense
5	+3.3V	6	GND-Sense (+3.3V)
7	+5V	8	+5V-Sense
9	GND	10	GND-Sense (+5V)



Pin	Signal
1	IPMB_SCL
2	GND
3	IPMB_SDA
4	IPMB_PWR
5	SMB_RSV

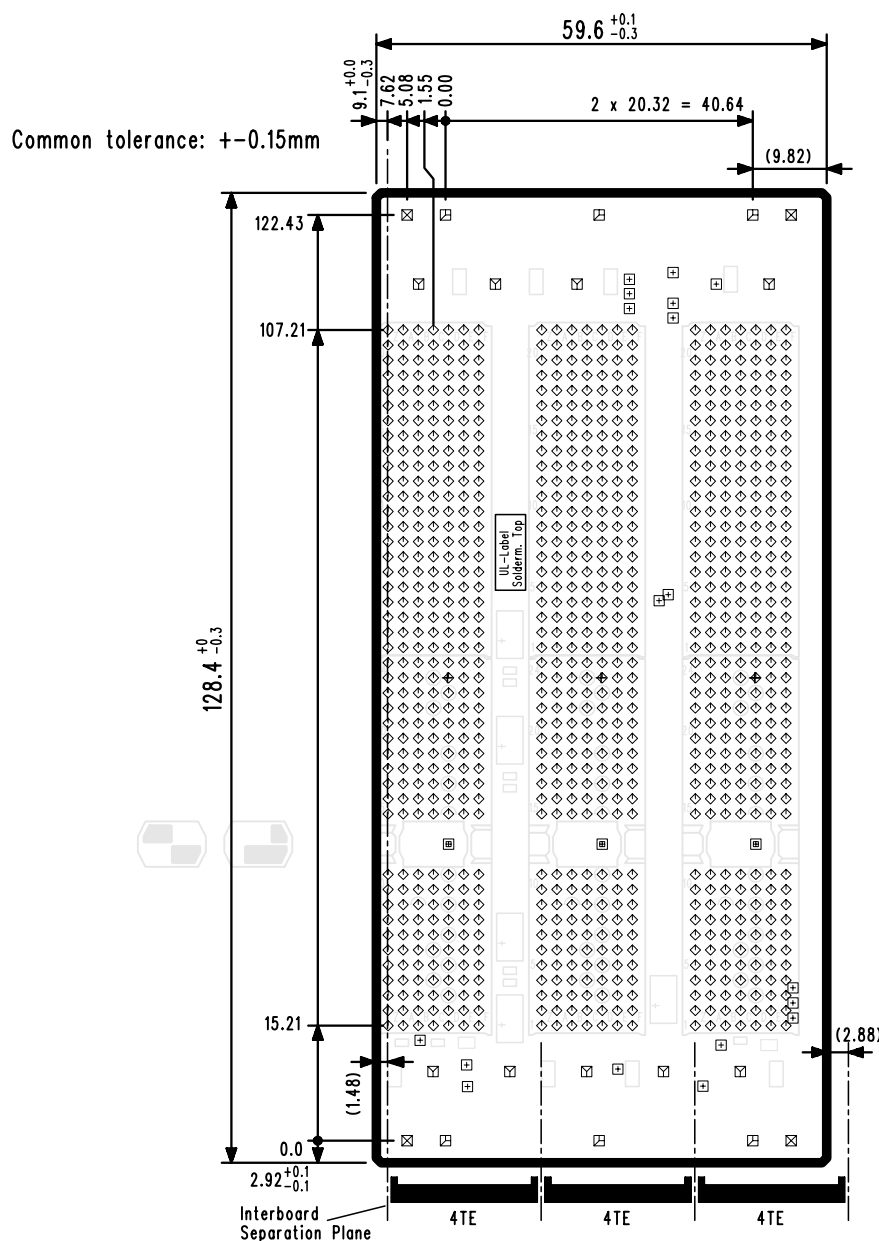
Pin	Signal
1	GND
2	TCK
3	TMS
4	TRST-
5	TDI
6	TDO

☐ SR5 ☐	☐ ☐ SR4 ☐
---	---

BU2675 Rev.BB
CPCI 3HE RA 32lot 32bit+
Hartmann Elektronik

Silkscreen Bottom
Pads Bottom

				2006	Date	Name	Title	Hartmann Elektronik CPCI 3HE RA 3Slot 32bit+ BUS675 Rev.BB				
				Author	12.09.	Wiedmann						
				Check	12.09.	Wiedmann						
				DIN A4								
				M 1:1								
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			Number	Sheet				
								of				
Rev	Description	Date	Name							Device:	HE-C33RA03---4	File: BUS675BB

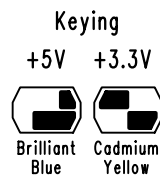
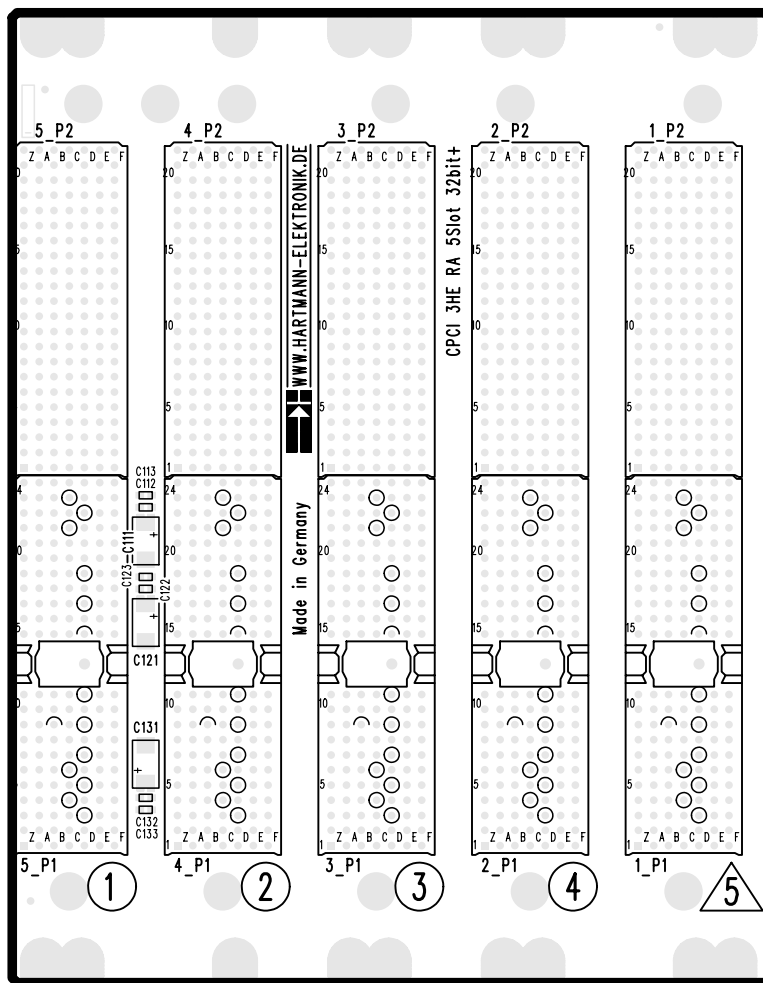


Hartmann Elektronik
CPCI 3HE RA 3Slot 32bit+
BUS675 Rev.BB

				2006	Date	Name	Title	Hartmann Elektronik CPCI 3HE RA 3Slot 32bit+ BUS675 Rev.BB		
				Author	12.09.	Wiedmann				
				Check	12.09.	Wiedmann				
				DIN A4 M 1:1						
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			Number		Sheet	
									of	
Rev	Description	Date	Name				Device:	HE-C33RA03---4	File:	BUS675BB

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TOP VIEW



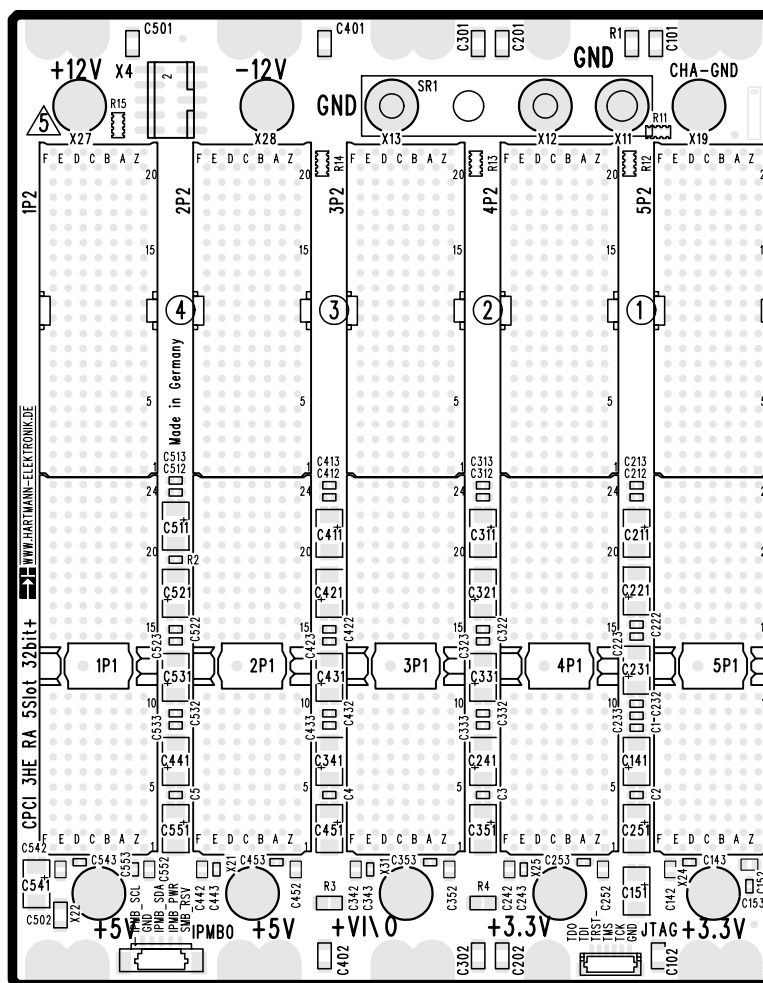
Hartmann Elektronik
CPCI 3HE RA 5Slot 32bit+
BUS673 Rev.BA

Silkscreen Top
Pads Top

				2000	Date	Name	Title Hartmann Elektronik CPCI 3HE RA 5Slot 32bit+ BUS673 Rev.BA		
				Author	10.11.	Ruthardt			
				Check	10.11.	Ruthardt			
				DIN A4 M 1:1					
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			Number		Sheet
									of
Rev	Description	Date	Name				Device: HE-C33RA055--4		File: BUS673BA

Without geographic addressing on request

BOTTOM VIEW



Shrouds
1P1 - 5P1
on request

Busbar for +5V keying

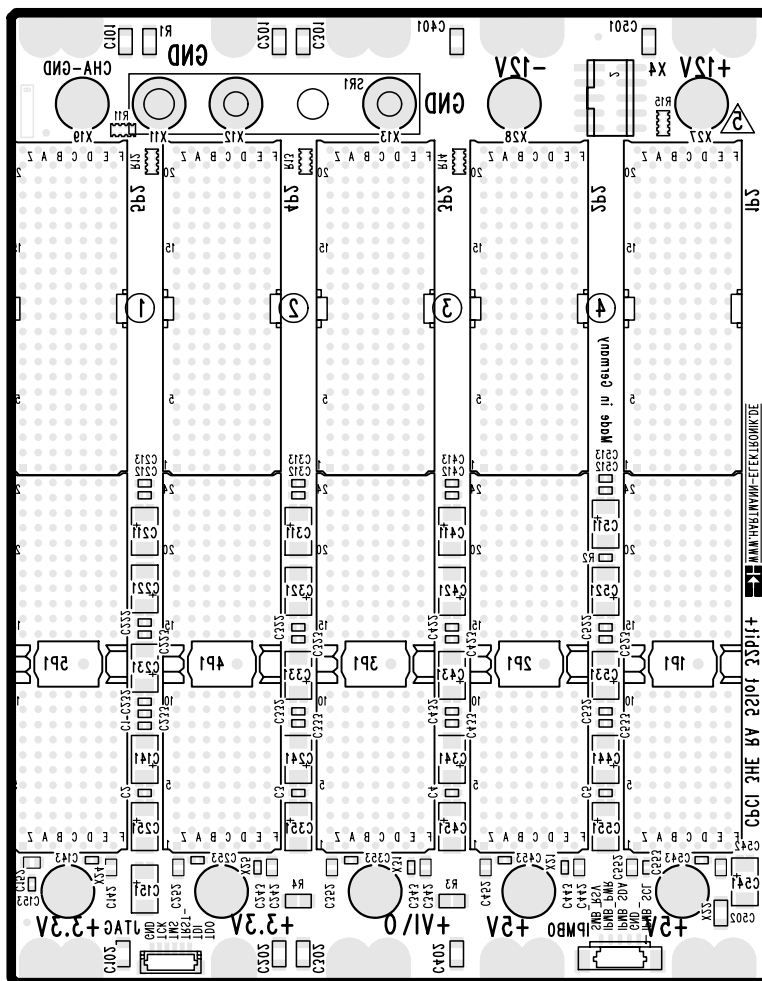
Busbar for +3.3V keying

Silkscreen Bottom
Pads Bottom

Hartmann Elektronik
CPC1 3HE RA 5Slot 32bit+
Rev.BA

				2000	Date	Name	Title Hartmann Elektronik CPCI 3HE RA 5Slot 32bit+ BUS673 Rev.BA						
				Author	10.11.	Ruthardt							
				Check	10.11.	Ruthardt							
				DIN A4 M 1:1									
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			Number	Sheet					
								of					
Rev	Description	Date	Name							Device:	HE-C33RA055--4	File:	BUS673BA

BOTTOM VIEW



2 rounds
191 - 192
on request

Busbar for +5V keying

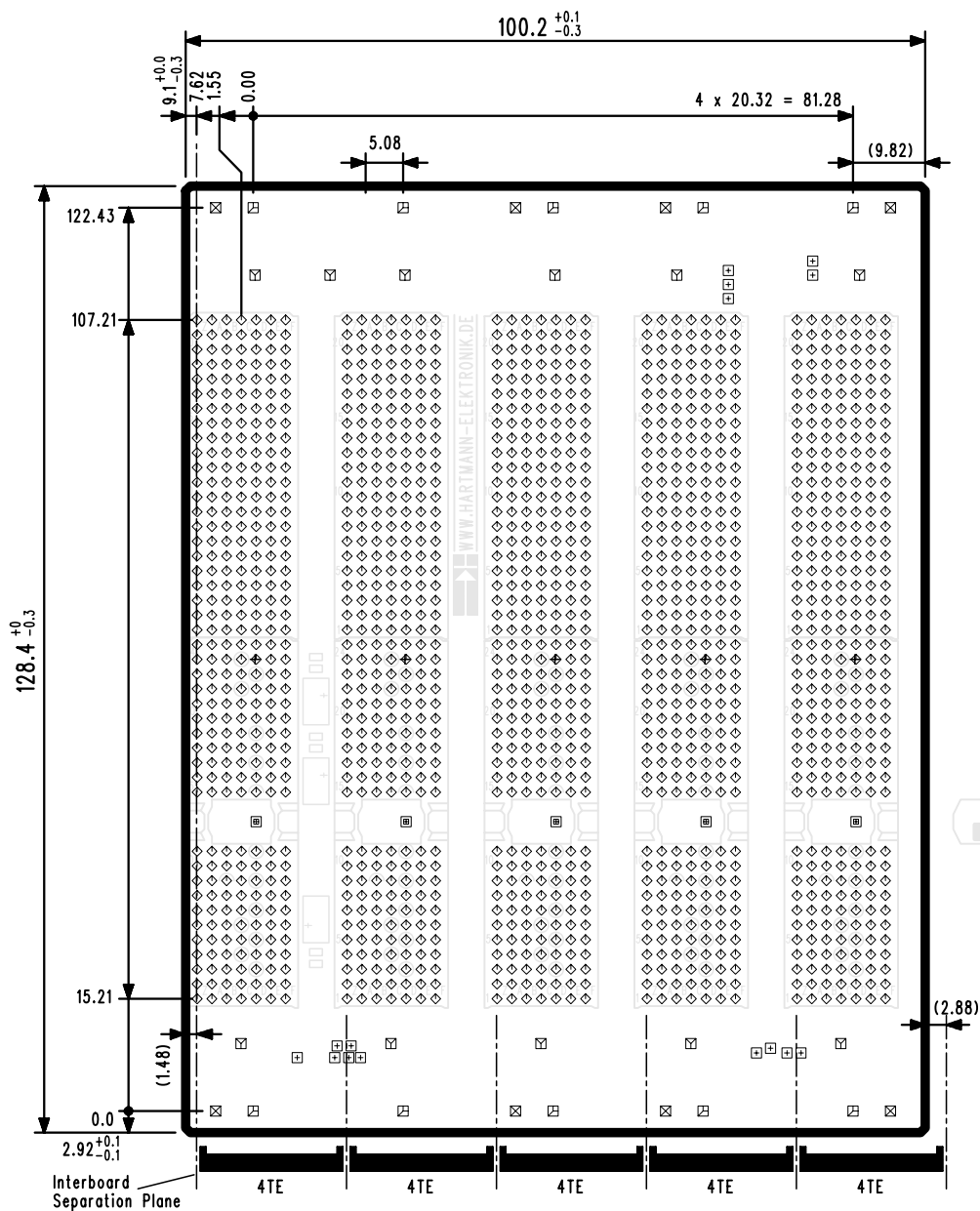
Busbar for +3.3V keying

Hartmann Elektronik
CPCI 3HE RA 5Slot 32bit+
BUS673 Rev.BA

Pods Bottom
Silkscreen Bottom

				2000	Date	Name	Title Hartmann Elektronik CPCI 3HE RA 5Slot 32bit+ BUS673 Rev.BA			
				Author	10.11.	Ruthardt				
				Check	10.11.	Ruthardt				
				DIN A4						
				M 1:1						
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			Number	Sheet		
								of		
Rev	Description	Date	Name				Device:	HE-C33RA055--4	File:	BUS673BA

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Hartmann Elektronik
CPCI 3HE RA 5Slot 32bit+
BUS673 Rev.BA

				2000	Date	Name	Title Hartmann Elektronik CPCI 3HE RA 5Slot 32bit+ BUS673 Rev.BA		
				Author	10.11.	Ruthardt			
				Check	10.11.	Ruthardt			
				DIN A4 M 1:1					
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			Number		Sheet
									of
Rev	Description	Date	Name				Device: HE-C33RA055--4		File: BUS673BA

+5V	+3.3V
	
Brilliant Blue	Cadmium Yellow

Silkscreen Top
Pads Top

Hartmann Elektronik			
Title		Date	Name
	2000		

[illegible]

Without geographic addressing on request

BOTTOM VIEW

Shrouds
1P1 - 6P1
on request

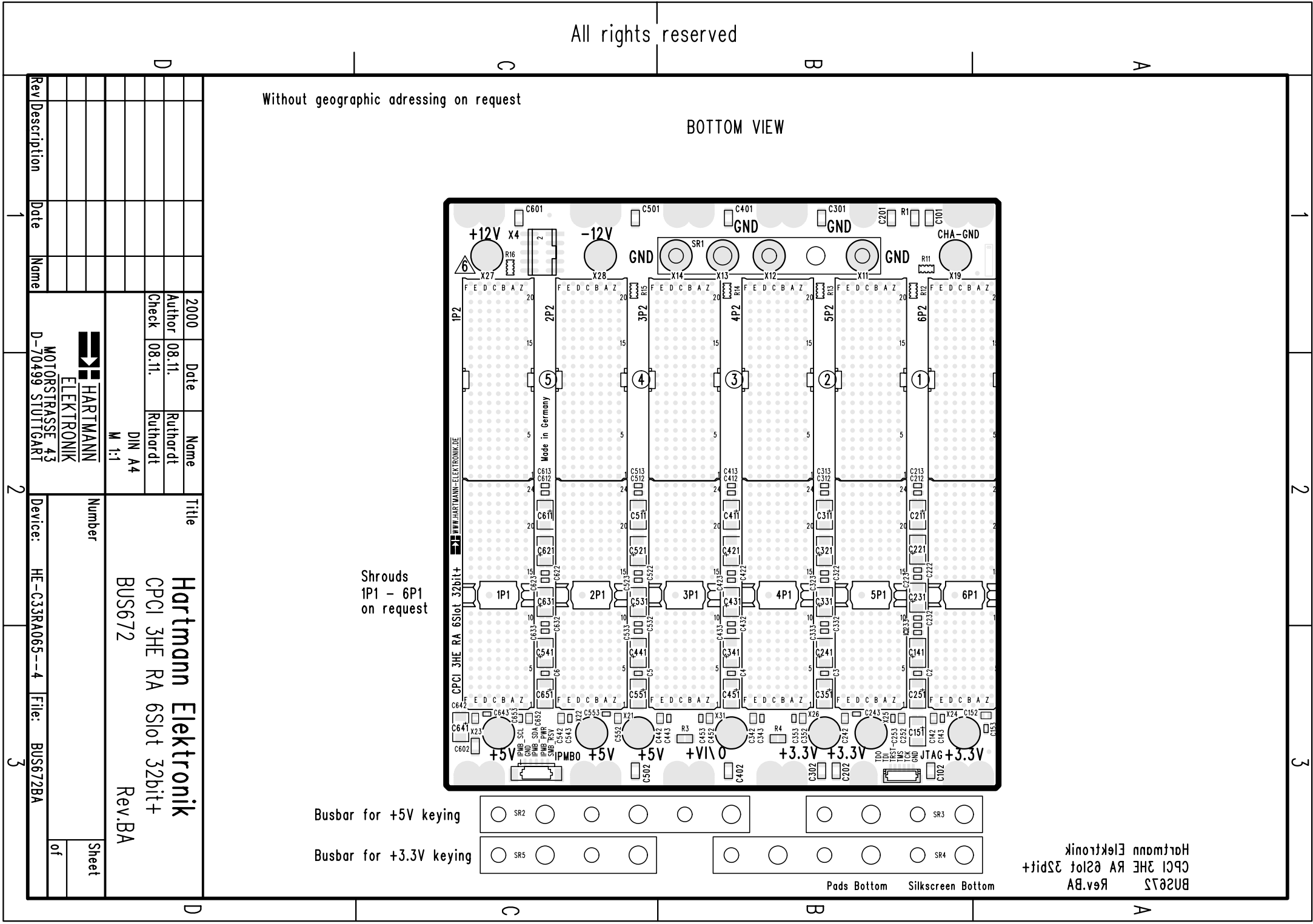
Busbar for +5V keying

Busbar for +3.3V keying

Pads Bottom Silkscreen Bottom

Rev.BA
CPCI 3HE RA 6Slot 32bit+
Hartmann Elektronik

Rev.BA
CPCI 3HE RA 6Slot 32bit+
Hartmann Elektronik

[illegible]

Without geographic addressing on request

1 2 3

A B C D

1 2 3

A B C D

Without geographic addressing on request

BOTTOM VIEW

Shrouds
1P1 - 6P1
on request

Busbar for +5V keying

Busbar for +3.3V keying

Pads Bottom Silkscreen Bottom

Rev	Description	Date	Name
1			

2000 Date Name

Author 08.11. Ruthardt

Check 08.11. Ruthardt

DIN A4 M 1:1

HARTMANN ELEKTRONIK

MOTORSTRASSE 43

D-70499 STUTTGART

Device: HE-C33RA065--4 File: BUS672BA

Sheet of

Hartmann Elektronik

CPCI 3HE RA 6Slot 32bit+

Rev.BA

BUS672

Rev.BA

Without geographic addressing on request

BOTTOM VIEW

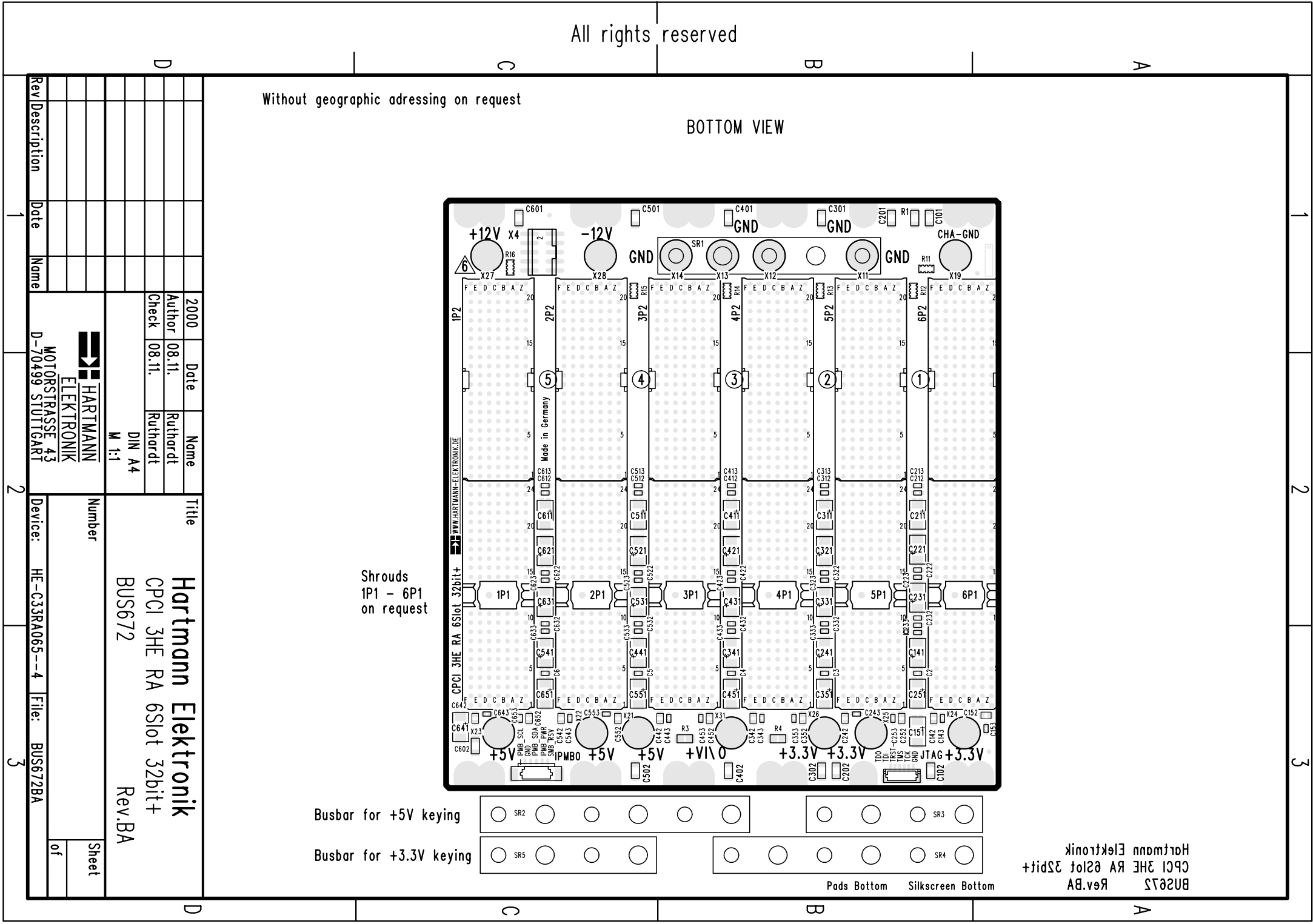
Shrouds
1P1 - 6P1
on request

Busbar for +5V keying

Busbar for +3.3V keying

Pads Bottom Silkscreen Bottom

Rev.BA
CPCI 3HE RA 6Slot 32bit+
Hartmann Elektronik

[illegible]

Without geographic addressing on request

BOTTOM VIEW

Shrouds
1P1 - 6P1
on request

Busbar for +5V keying

Busbar for +3.3V keying

Pads Bottom Silkscreen Bottom

Rev.BA
CPCI 3HE RA 6Slot 32bit+
Hartmann Elektronik

[illegible]

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Technical drawing of a PCB layout for Hartmann Elektronik. The drawing shows a rectangular board with dimensions 120.5 (+0.1/-0.3) mm by 128.4 (+0/-0.3) mm. The layout includes a central area with a grid of holes, surrounded by a border. Dimensions for the border and central area are provided. The drawing includes a title block with company information, a revision table, and a sheet number.

Dimensions:

- Overall width: 120.5 (+0.1/-0.3) mm
- Overall height: 128.4 (+0/-0.3) mm
- Top border: 5.08 mm
- Bottom border: 2.92 (+0.1/-0.1) mm
- Left border: 122.43 mm
- Right border: 15.21 mm
- Central area width: 107.21 mm
- Central area height: 107.21 mm
- Top border offset: 9.1 (+0.0/-0.3) mm
- Top border offset: 7.62 mm
- Top border offset: 1.55 mm
- Top border offset: 0.00 mm
- Bottom border offset: 1.48 mm
- Bottom border offset: (9.82) mm
- Bottom border offset: (2.88) mm

Company Information:

Hartmann Elektronik
CPCI 3HE RA 6Slot 32bit+
BUS672
Rev.BA

Revision Table:

Rev	Description	Date	Name
1			
2			
3			

Sheet Information:

Sheet 1 of 1

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Hartmann Elektronik
CPCI 3HE RA 6Slot 32bit+
BUS672 Rev.BA

Hartmann Elektronik
CPCI 3HE RA 6Slot 32bit+
BUS672 Rev.BA

Rev	Description	Date	Name
1			

2000	Date	Name
Author	08.11.	Ruthardt
Check	08.11.	Ruthardt

DIN A4
M 1:1

Number

Sheet
of

Device: HE-C33RA065--4 File: BUS672BA

Revised By: Rev.BA

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120.5^{+0.1}_{-0.3}

5 x 20.32 = 101.6

(9.82)

5.08

122.43

107.21

15.21

0.0

2.92^{+0.1}_{-0.1}

Interboard Separation Plane

4TE

4TE

4TE

4TE

4TE

4TE

128.4⁺⁰_{-0.3}

9.1^{+0.0}_{-0.3}

7.62

1.55

0.00

WWW.HARTMANN-ELEKTRONIK.DE

HARTMANN ELEKTRONIK

2000

Date

08.11.

Author

Ruthardt

Check

08.11.

DIN A4

M 1:1

Hartmann Elektronik

CPCI 3HE RA 6Slot 32bit+

BUS672

Rev.BA

Rev	Description	Date	Name
1			
2			
3			

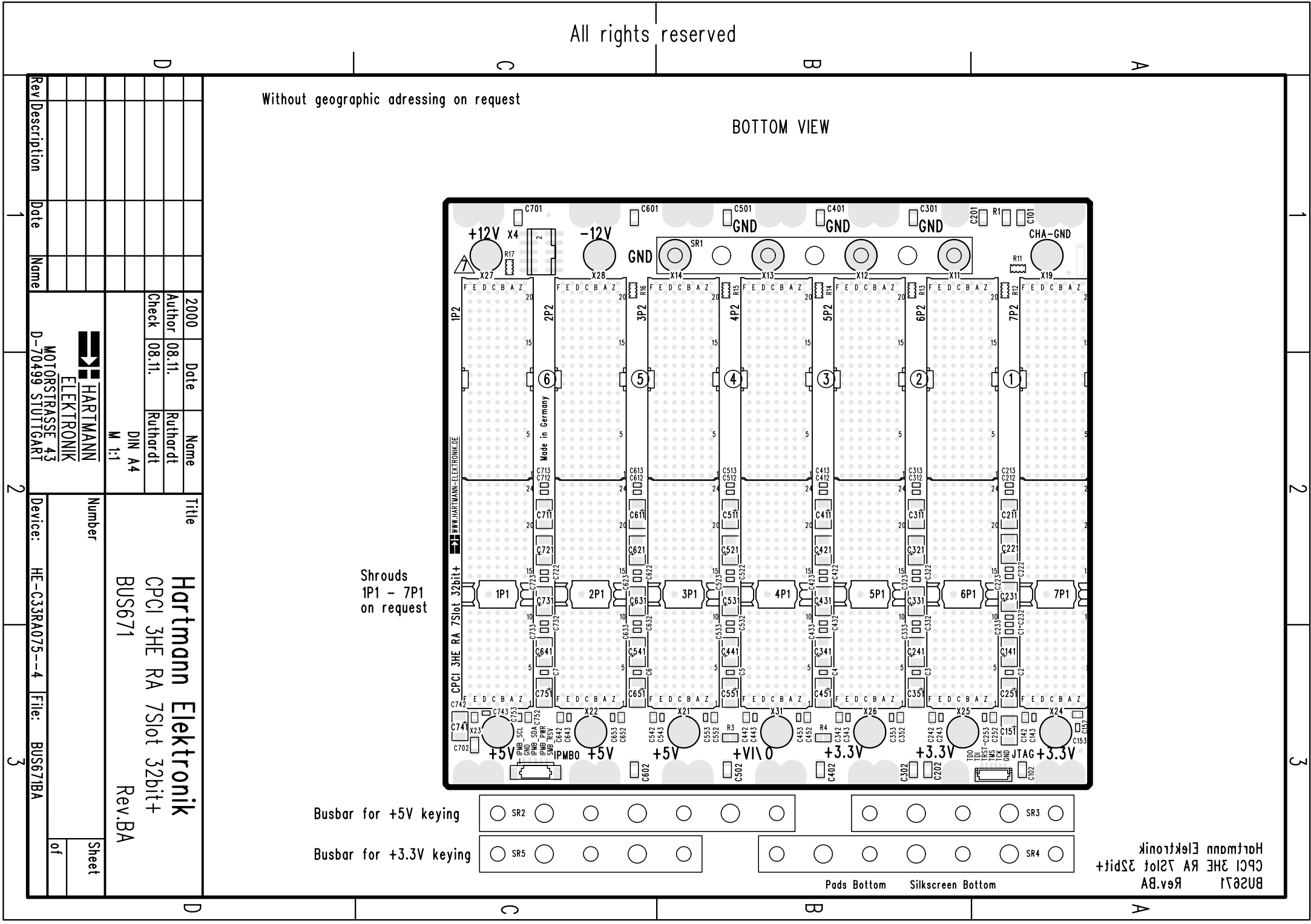
Number	Sheet	of

Device: HE-C33RA065--4

File: BUS672BA

Hartmann Elektronik
CPCI 3HE RA 7Slot 32bit+
BUS671 Rev.BA

[illegible]

[illegible][illegible]

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BOTTOM VIEW

Shrouds
1P1 - 7P1
on request

Busbar for +5V keying

Busbar for +3.3V keying

Pads Bottom Silkscreen Bottom

Bus671 Rev.BA
CPCI 3HE RA 7Slot 32bit+
Hartmann Elektronik

Without geographic addressing on request

Rev	Description	Date	Name	2000	Date	Name	Title	Number	Sheet
1									1 of
2									2 of
3									3 of
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									70 of

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BOTTOM VIEW

Shrouds
1P1 - 7P1
on request

Busbar for +5V keying

Busbar for +3.3V keying

Pads Bottom Silkscreen Bottom

Busbar for +5V keying
Busbar for +3.3V keying

Without geographic addressing on request

Rev	Description	Date	Name	2000	Date	Name	Title	Number	Sheet
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64									64
65									65

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BOTTOM VIEW

Shrouds
1P1 - 7P1
on request

Busbar for +5V keying

Busbar for +3.3V keying

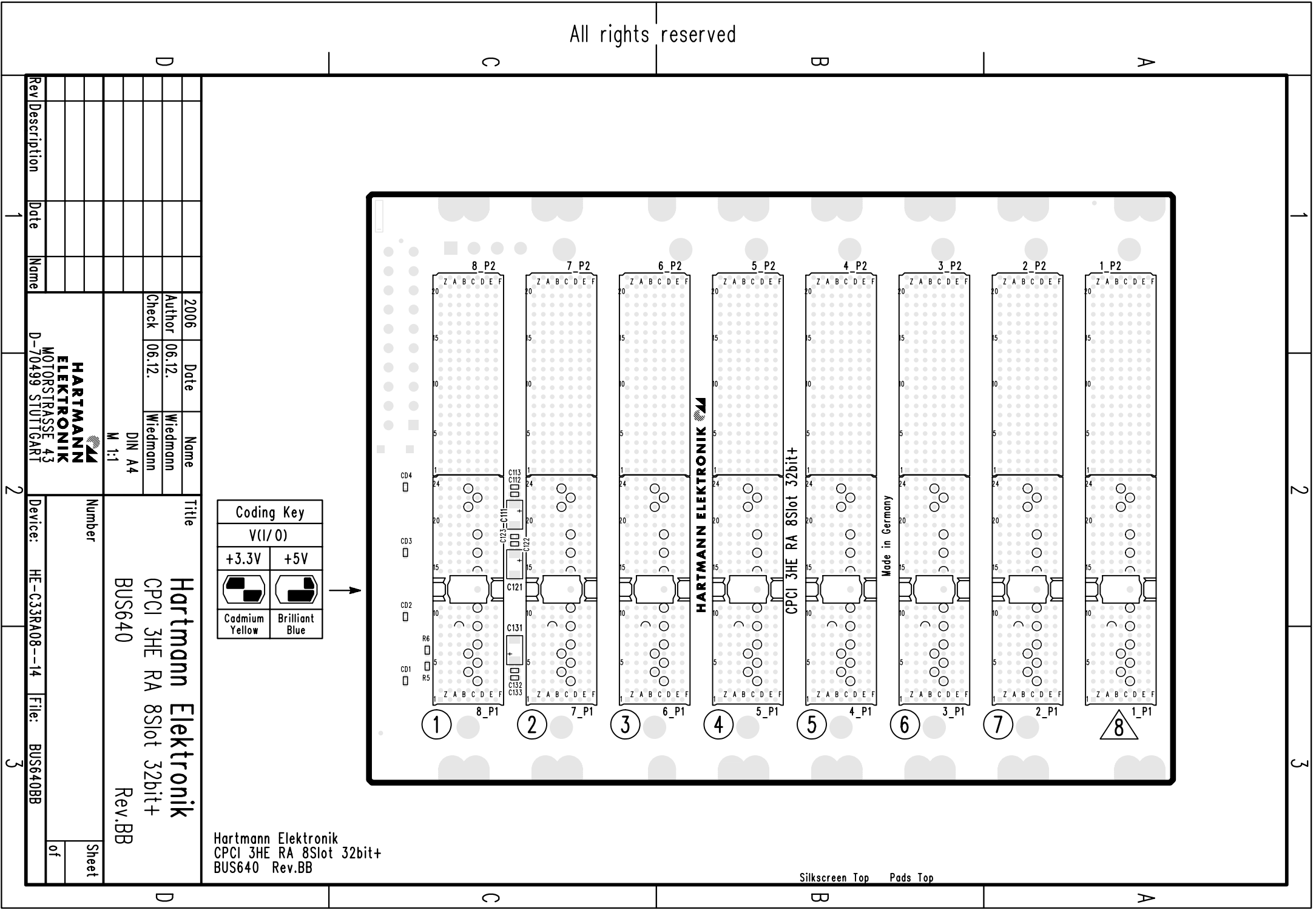
Pads Bottom Silkscreen Bottom

Bus671 Rev.BA
CPCI 3HE RA 7Slot 32bit+
Hartmann Elektronik

Without geographic addressing on request

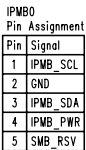
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									65 of
									66 of
									67 of
									68 of

[illegible][illegible][illegible]



JTAG Pin Assignment

Pin	Signal
1	GND
2	TCK
3	TMS
4	TRST-
5	TDI
6	TDO



BU2640 Rev.BB
CPCI 3HE RA 8Slot 32bit+
Hartmann Elektronik

Sheet	
of	

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1 2 3

A B C D

175.2^{+0.1}_{-0.3}

7 x 20.32 = 142.24

(9.82)

5.08

23.2^{+0.0}_{-0.3}

7.62

1.55

0.00

122.43

107.21

128.4^{+0.0}_{-0.1}

15.21

(9.74)

2.92^{+0.1}_{-0.1}

0.0

4TE

4TE

4TE

4TE

4TE

4TE

4TE

4TE

4TE

4TE

Interboard Separation Plane

Top Layer - UI - Soldermask

Common Tolerance: ± 0.15 mm

Hartmann Elektronik
CPCI 3HE RA 8Slot 32bit+
BUS640 Rev.BB

2006 Date Name
Author 06.12. Wiedmann
Check 06.12. Wiedmann
DIN A4
M 1:1

Rev Description Date Name

1

2

3

Device: HE-C33RA08--14 File: BUS640BB

Sheet of

[illegible]

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The drawing is a technical representation of a printed circuit board (PCB) layout. It includes a title block with project information, a detailed component list table, and a top-down view of the board with dimensions and component footprints.

Table 1: Component List

Rev	Description	Date	Name	2006	Date	Name	Title
1				Author	06.12.	Wiedmann	Hartmann Elektronik CPCI 3HE RA 8Slot 32bit+ BUS640 Rev.BB
				Check	06.12.	Wiedmann	
						DIN A4 M 1:1	
							Number
							Device: HE-C33RA08--14
							File: BUS640BB
							Sheet
							of

Table 2: Component List

Rev	Description	Date	Name	2006	Date	Name	Title
1				Author	06.12.	Wiedmann	Hartmann Elektronik CPCI 3HE RA 8Slot 32bit+ BUS640 Rev.BB
				Check	06.12.	Wiedmann	
						DIN A4 M 1:1	
							Number
							Device: HE-C33RA08--14
							File: BUS640BB
							Sheet
							of

Table 3: Component List

Rev	Description	Date	Name	2006	Date	Name	Title
1				Author	06.12.	Wiedmann	Hartmann Elektronik CPCI 3HE RA 8Slot 32bit+ BUS640 Rev.BB
				Check	06.12.	Wiedmann	
						DIN A4 M 1:1	
							Number
							Device: HE-C33RA08--14
							File: BUS640BB
							Sheet
							of

Table 4: Component List

Rev	Description	Date	Name	2006	Date	Name	Title
1				Author	06.12.	Wiedmann	Hartmann Elektronik CPCI 3HE RA 8Slot 32bit+ BUS640 Rev.BB
				Check	06.12.	Wiedmann	
						DIN A4 M 1:1	
							Number
							Device: HE-C33RA08--14
							File: BUS640BB
							Sheet
							of

Table 5: Component List

Rev	Description	Date	Name	2006	Date	Name	Title
1				Author	06.12.	Wiedmann	Hartmann Elektronik CPCI 3HE RA 8Slot 32bit+ BUS640 Rev.BB
				Check	06.12.	Wiedmann	
						DIN A4 M 1:1	
							Number
							Device: HE-C33RA08--14
							File: BUS640BB
							Sheet
							of

Table 6: Component List

Rev	Description	Date	Name	2006	Date	Name	Title
1				Author	06.12.	Wiedmann	Hartmann Elektronik CPCI 3HE RA 8Slot 32bit+ BUS640 Rev.BB
				Check	06.12.	Wiedmann	
						DIN A4 M 1:1	
							Number
							Device: HE-C33RA08--14
							File: BUS640BB
							Sheet
							of

Table 7: Component List

Rev	Description	Date	Name	2006	Date	Name	Title
1				Author	06.12.	Wiedmann	Hartmann Elektronik CPCI 3HE RA 8Slot 32bit+ BUS640 Rev.BB
				Check	06.12.	Wiedmann	
						DIN A4 M 1:1	
							Number
							Device: HE-C33RA08--14
							File: BUS640BB
							Sheet
							of

Table 8: Component List

Rev	Description	Date	Name	2006	Date	Name	Title
1				Author	06.12.	Wiedmann	Hartmann Elektronik CPCI 3HE RA 8Slot 32bit+ BUS640 Rev.BB
				Check	06.12.	Wiedmann	
						DIN A4 M 1:1	
							Number
							Device: HE-C33RA08--14
							File: BUS640BB
							Sheet
							of

Table 9: Component List

Rev	Description	Date	Name	2006	Date	Name	Title
1				Author	06.12.	Wiedmann	Hartmann Elektronik CPCI 3HE RA 8Slot 32bit+ BUS640 Rev.BB
				Check	06.12.	Wiedmann	
						DIN A4 M 1:1	
							Number

[illegible][illegible]

D

Systemslot



				2000	Date	Name	CompactPCI® PICMG 2.0 R3.0 Backplane 3HE RA 11Slot 32bit 33MHz
				Author	18.12.	Wiedmann	
				Check	18.12	Brosowski	
				DIN A3 M 1:1			
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			
Rev	Description	Date	Name	Order No.:			Sheet 1 of 2
				+5V: 33RA113514			Device: BUS659BA+BUS654
							File: HE-C33RA114-14

A

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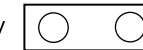
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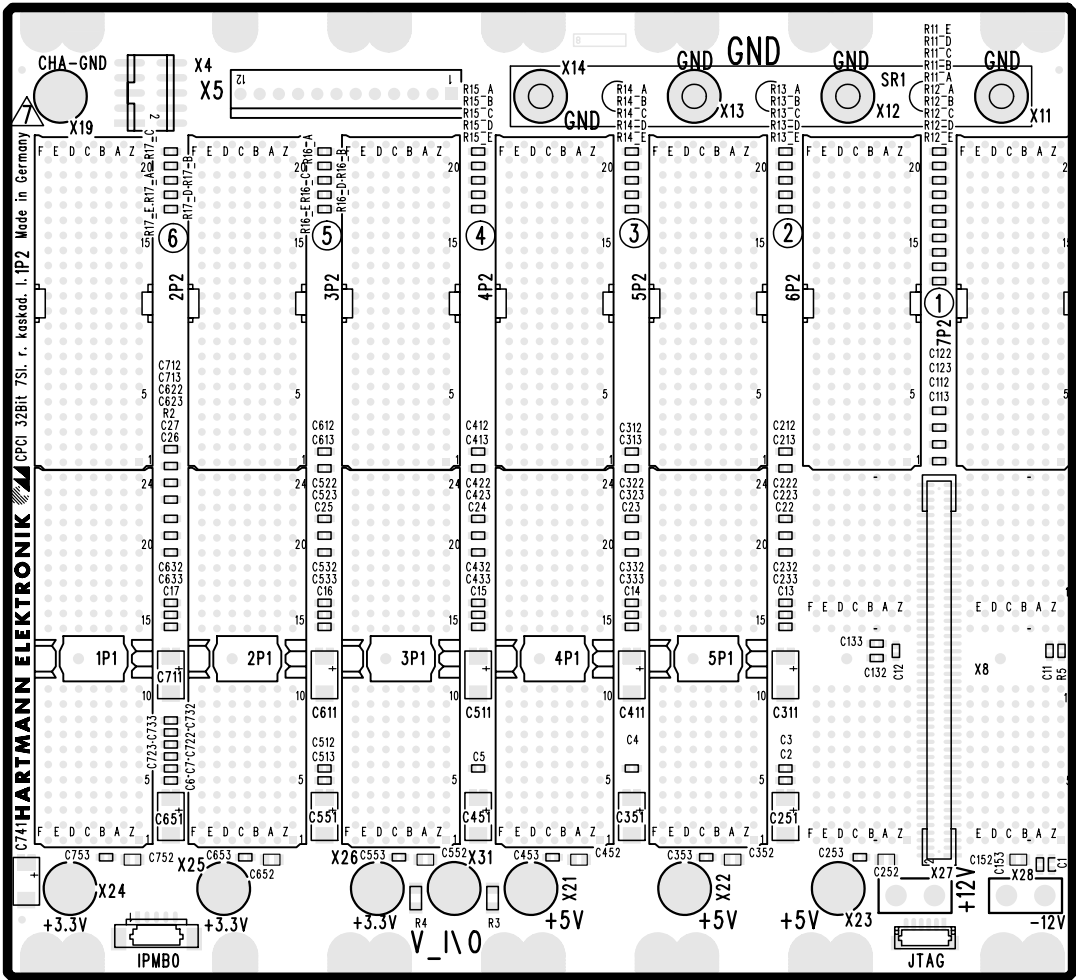
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				2000	Date	Name	CompactPCI® PICMG 2.0 R3.0 Backplane 3HE RA 11Slot 32bit 33MHz
				Author	18.12.	Wiedmann	
				Check	18.12	Brosowski	
				DIN A3 M 1:1			
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			
Rev	Description	Date	Name	Order No.:			Sheet 2 of 2
				+5V: 33RA113514			Device: BUS659BA+BUS654

X4 Pin Assignment			
Pin	Signal	Pin	Signal
1	PRST-	2	FAL-
3	DEG-	4	+3.3V-Sense
5	+3.3V	6	GND-Sense (+3.3V)
7	+5V	8	+5V-Sense
9	GND	10	GND-Sense (+5V)

BOTTOM VIEW



Without geographic addressing on request

Shrouds
1P1 - 5P1
on request

JTAG Pin Assignment	
Pin	Signal
1	GND
2	TCK
3	TMS
4	TRST-
5	TDI
6	TDO

IPMB0 Pin Assignment	
Pin	Signal
1	IPMB_SCL
2	GND
3	IPMB_SDA
4	IPMB_PWR
5	SMB_RSV

Busbar V(I/O) = 3.3V

Busbar V(I/O) = 5V

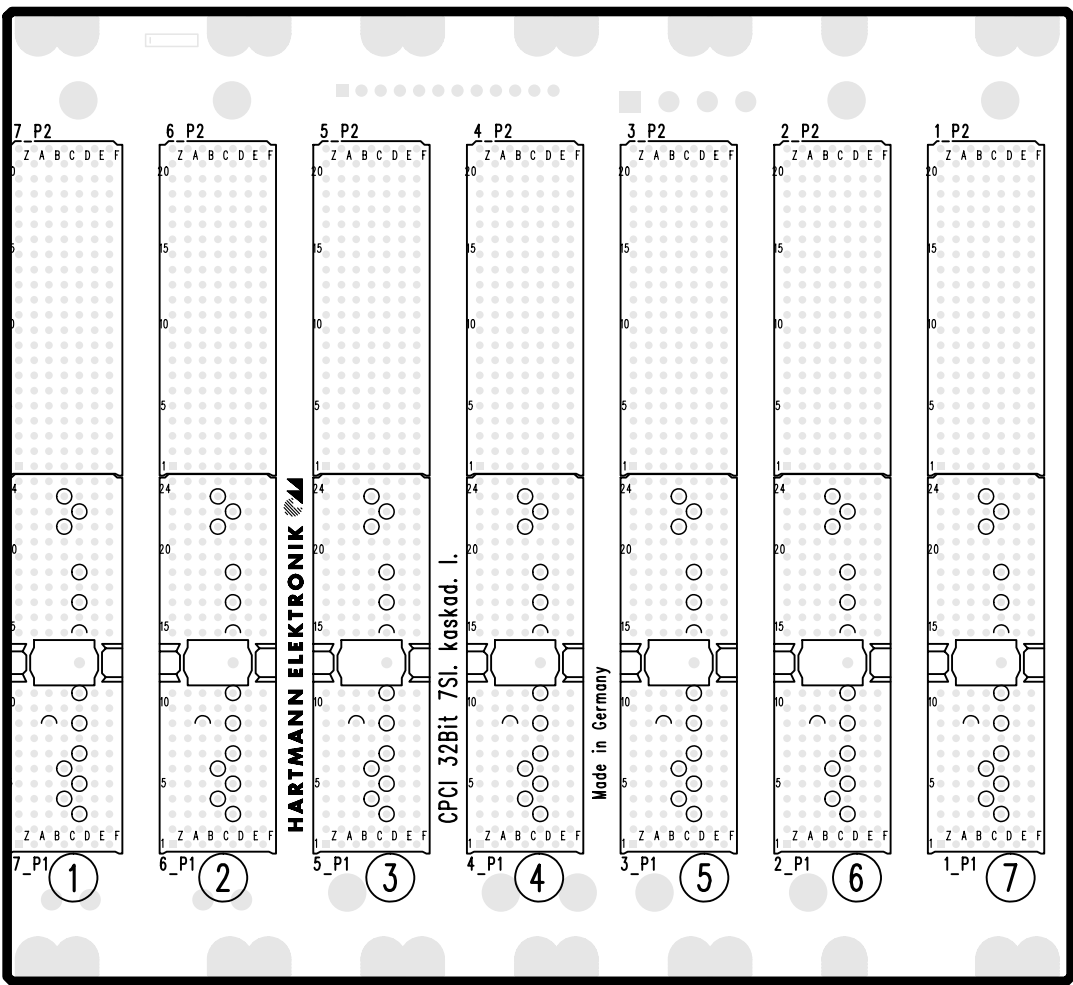
Pods L-Seite
Beschriftungsdruck L-Seite

Rev.BB
BUS659
CPCI 32Bit 7Sl. r. kaskad. l.
Hartmann Elektronik

Rev Description		Date	Name	Title		Number	File: BUS659BB	Sheet	of
1		2007	26.02.	Wiedmann	Hartmann Elektronik		CPCI 32Bit 7Sl. r. kaskad. l.	1	1
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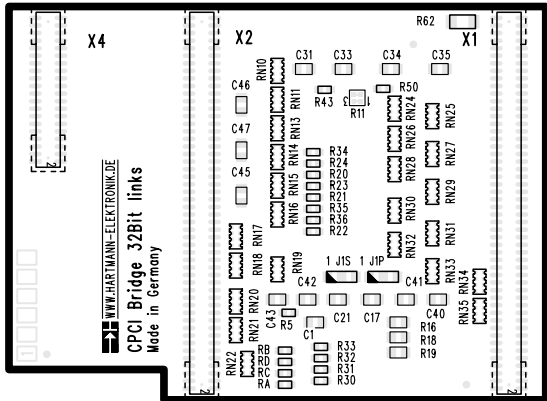
Coding Key	
V(I/O)	
+3.3V	+5V
	
Cadmium Yellow	Brilliant Blue

Beschriftungsdruck B-Seite
Pads B-Seite



Hartmann Elektronik
CPCI 32Bit 7Sl. kaskad. I.
BUS651
Rev.BB

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Alle Rechte vorbehalten	A							A																																																								
	B							B																																																								
	C	Hartmann Elektronik CPCI Bridge 32Bit links BUS653 Rev.01 Beschriftungsdruck B-Seite Pads B-Seite						C																																																								
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							zu Gerät:																																																									
							Datei: BUS653A																																																									
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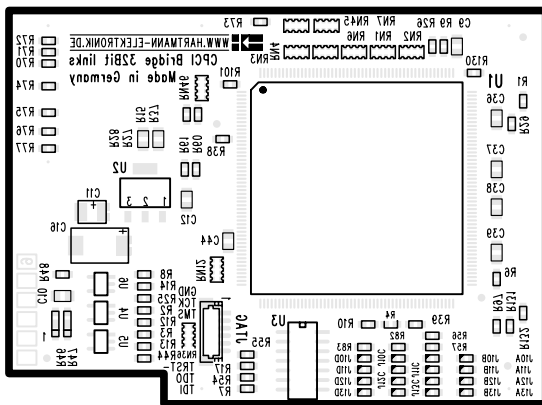
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Hartmann Elektronik
Beschäftigungsdruck L-2eife
CPCI Bridge 32Bit links
BUS653
Rev.01

C

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				2002	Tag	Name	Benennung Hartmann Elektronik CPCI Bridge 32Bit links BUS653 Rev.01				
				Bearb.	09.04.	Hamann					
				Gepr.	09.04.	Keko					
				UNTERLAGENSATZ DIN A4 M 1:1							
				 HARTMANN ELEKTRONIK MOTORSTRASSE 43 D-70499 STUTTGART			Zeich.-Nr.		Blatt		
											v.
							zu Gerät:			Datei: BUS653A	
Rev	Änderungs-Nr	Tag	Name								

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