

6U OpenVPX BACKPLANE

BKP6-CEN10-11.2.6-3

VITA 46
VITA 65

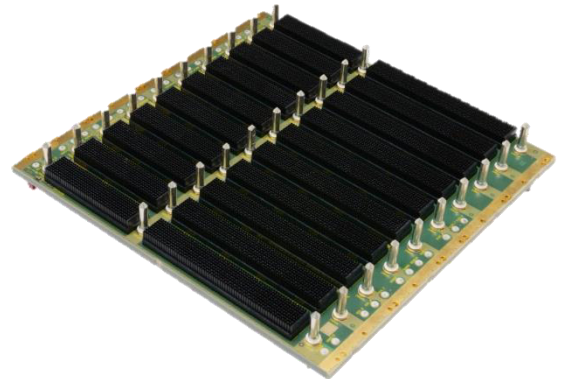


Key Features:

- Compliant to VITA 46.0 baseline specification
- Compliant to VITA 65 , BKP6-CEN10-11.2.6-3
- 10 Slots VPX, 8 Payload Slots , 2 Switch Slots
- Configuration Data plane: Data Star for 4 slots (2xFP/Slot)
- Configuration Control plane: Single Star (2x UTP/Slot)
- M3 studs for power entry
- PCB size 261.85mm x 253.23mm x 5.4 mm
- 5 HP from slot to slot (25.40 mm)
- Flexible keying and alignment mechanism
- with geographical address pins
- System Management Interface on the backplane (I2CA, I2CB)
- with JTAG connector on first slot (JT1)
- Reference clock
- Auxiliary clock
- Non-Volatile Memory Read Only signal set by Jumper BR1
- Battery backup option setting by Jumper XBAT. Vbat external or connected to 3.3 AUX.
- Max. Input current per backplane
VS1:VS2:VS3 = 240A : 240A : 220A
(14A/ Slot and Voltage)
- System Reset
- Operating temperature: -40° - +85°C
- Storage temperature: -55°C - +85°C
- Flammability rating: UL94-V0
- Custom assembly or modification on request

▪ **Order number: B196821061**

Front side



Rear side



6U OpenVPX BACKPLANE
BKP6-CEN10-11.2.6-3

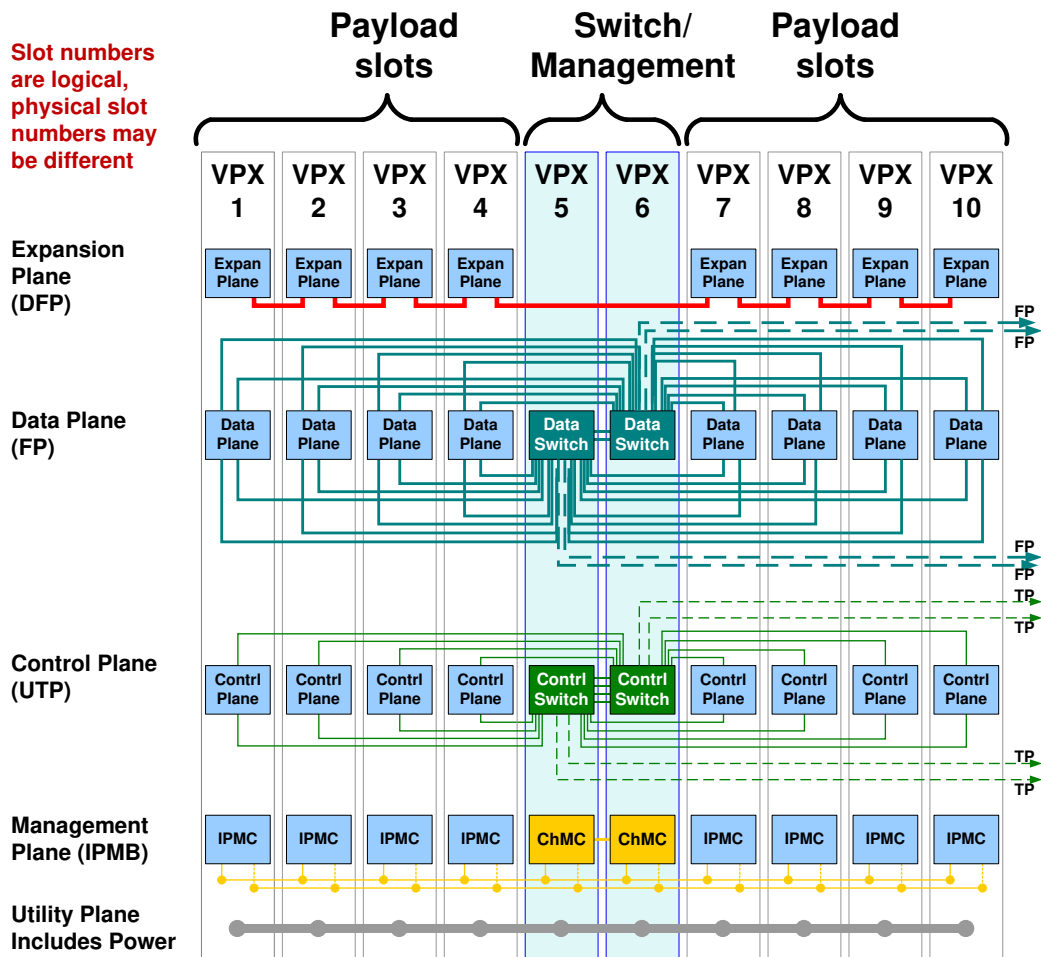
VITA 46
VITA 65



1) Topology: 10-Slot — BKP6-CEN10-11.2.6-3 (8 Payload + 2 Switch)

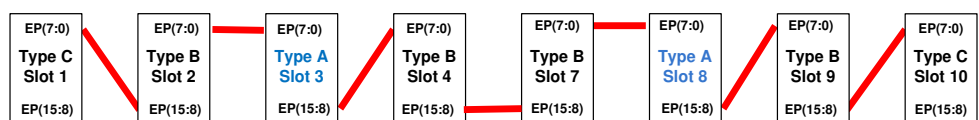
Profile Switch slot: SLT6-SWH-16U20F-10.4.2 / MOD6-SWH-16U20F-12.4.2-n

Profile Payload slot: SLT6-PAY-4F1Q2U2T-10.2.1 / Mod6-PAY-4F1Q2U2T-12.2.1-n



Expansion Plane Lanes of BKP6-CEN10-11.2.6-n

Expansion Plane lanes Payload slots of 10-slot backplane (DFP = 8 lanes)



6U OpenVPX BACKPLANE
BKP6-CEN10-11.2.6-3

VITA 46
VITA 65

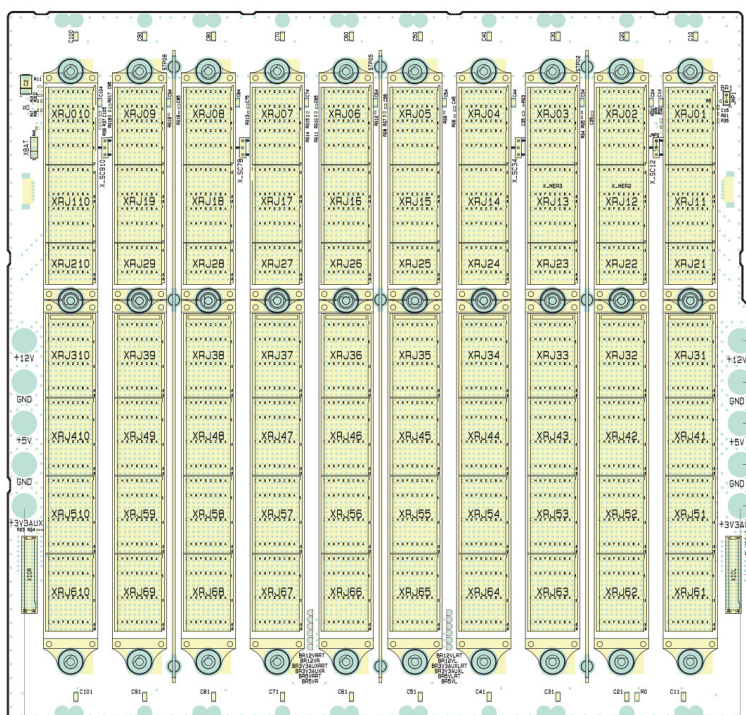


2) Drawings:

Front side



Back side



6U OpenVPX BACKPLANE BKP6-CEN10-11.2.6-3

VITA 46
VITA 65



2) Pin Assignment

Payload Slot Profile SLT6-PAY-4F1Q2U2T-10.2.1— P1 & J1

Plug-In	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P1			Even	Odd			Even	Odd	
Bplane J1	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	GDDiscrete1	GND	GND-J1	DP01-T0-	DP01-T0+	GND	GND-J1	DP01-R0-	DP01-R0+
2	GND	DP01-T1-	DP01-T1+	GND-J1	GND	DP01-R1-	DP01-R1+	GND-J1	GND
3	P1-VBAT	GND	GND-J1	DP01-T2-	DP01-T2+	GND	GND-J1	DP01-R2-	DP01-R2+
4	GND	DP01-T3-	DP01-T3+	GND-J1	GND	DP01-R3-	DP01-R3+	GND-J1	GND
5	SYS_CON*	GND	GND-J1	DP02-T0-	DP02-T0+	GND	GND-J1	DP02-R0-	DP02-R0+
6	GND	DP02-T1-	DP02-T1+	GND-J1	GND	DP02-R1-	DP02-R1+	GND-J1	GND
7	Reserved	GND	GND-J1	DP02-T2-	DP02-T2+	GND	GND-J1	DP02-R2-	DP02-R2+
8	GND	DP02-T3-	DP02-T3+	GND-J1	GND	DP02-R3-	DP02-R3+	GND-J1	GND
9	UD	GND	GND-J1	DP03-T0-	DP03-T0+	GND	GND-J1	DP03-R0-	DP03-R0+
10	GND	DP03-T1-	DP03-T1+	GND-J1	GND	DP03-R1-	DP03-R1+	GND-J1	GND
11	UD	GND	GND-J1	DP03-T2-	DP03-T2+	GND	GND-J1	DP03-R2-	DP03-R2+
12	GND	DP03-T3-	DP03-T3+	GND-J1	GND	DP03-R3-	DP03-R3+	GND-J1	GND
13	UD	GND	GND-J1	DP04-T0-	DP04-T0+	GND	GND-J1	DP04-R0-	DP04-R0+
14	GND	DP04-T1-	DP04-T1+	GND-J1	GND	DP04-R1-	DP04-R1+	GND-J1	GND
15	Maskable Reset*	GND	GND-J1	DP04-T2-	DP04-T2+	GND	GND-J1	DP04-R2-	DP04-R2+
16	GND	DP04-T3-	DP04-T3+	GND-J1	GND	DP04-R3-	DP04-R3+	GND-J1	GND

Payload Slot Profile SLT6-PAY-4F1Q2U2T-10.2.1— P2 & J2

Plug-In	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P2			Even	Odd			Even	Odd	
Backplane J2	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J2	EP00-T-	EP00-T+	GND	GND-J2	EP00-R-	EP00-R+
2	GND	EP01-T-	EP01-T+	GND-J2	GND	EP01-R-	EP01-R+	GND-J2	GND
3	UD	GND	GND-J2	EP02-T-	EP02-T+	GND	GND-J2	EP02-R-	EP02-R+
4	GND	EP03-T-	EP03-T+	GND-J2	GND	EP03-R-	EP03-R+	GND-J2	GND
5	UD	GND	GND-J2	EP04-T-	EP04-T+	GND	GND-J2	EP04-R-	EP04-R+
6	GND	EP05-T-	EP05-T+	GND-J2	GND	EP05-R-	EP05-R+	GND-J2	GND
7	UD	GND	GND-J2	EP06-T-	EP06-T+	GND	GND-J2	EP06-R-	EP06-R+
8	GND	EP07-T-	EP07-T+	GND-J2	GND	EP07-R-	EP07-R+	GND-J2	GND
9	UD	GND	GND-J2	EP08-T-	EP08-T+	GND	GND-J2	EP08-R-	EP08-R+
10	GND	EP09-T-	EP09-T+	GND-J2	GND	EP09-R-	EP09-R+	GND-J2	GND
11	UD	GND	GND-J2	EP10-T-	EP10-T+	GND	GND-J2	EP10-R-	EP10-R+
12	GND	EP11-T-	EP11-T+	GND-J2	GND	EP11-R-	EP11-R+	GND-J2	GND
13	UD	GND	GND-J2	EP12-T-	EP12-T+	GND	GND-J2	EP12-R-	EP12-R+
14	GND	EP13-T-	EP13-T+	GND-J2	GND	EP13-R-	EP13-R+	GND-J2	GND
15	UD	GND	GND-J2	EP14-T-	EP14-T+	GND	GND-J2	EP14-R-	EP14-R+
16	GND	EP15-T-	EP15-T+	GND-J2	GND	EP15-R-	EP15-R+	GND-J2	GND

6U OpenVPX BACKPLANE

BKP6-CEN10-11.2.6-3

VITA 46
VITA 65



Payload Slot Profile SLT6-PAY-4F1Q2U2T-10.2.1— P3 & J3

Plug-In	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P3			Even	Odd			Even	Odd	
Backplane J3	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a

This connector is all User Defined pins.

Payload Slot Profile SLT6-PAY-4F1Q2U2T-10.2.1— P4 & J4

Plug-In	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Mod P4			Even	Odd			Even	Odd	
Bplane J4	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
2	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
3	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
4	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
5	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
6	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
7	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
8	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
9	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
10	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
11	UD	GND	GND-J4	CP utp02-T-	CP utp02-T+	GND	GND-J4	CP utp02-R-	CP utp02-R+
12	GND	CP utp01-T-	CP utp01-T+	GND-J4	GND	CP utp01-R-	CP utp01-R+	GND-J4	GND
13	UD	GND	GND-J4	CP tp02-DB-	CP tp02-DB+	GND	GND-J4	CP tp02-DA-	CP tp02-DA+
14	GND	CP tp02-DD-	CP tp02-DD+	GND-J4	GND	CP tp02-DC-	CP tp02-DC+	GND-J4	GND
15	UD	GND	GND-J4	CP tp01-DB-	CP tp01-DB+	GND	GND-J4	CP tp01-DA-	CP tp01-DA+
16	GND	CP tp01-DD-	CP tp01-DD+	GND-J4	GND	CP tp01-DC-	CP tp01-DC+	GND-J4	GND

6U OpenVPX BACKPLANE BKP6-CEN10-11.2.6-3	VITA 46 VITA 65	
---	--------------------	--

Payload Slot Profile SLT6-PAY-4F1Q2U2T-10.2.1— P5 & J5

Plug-In	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P5			Even	Odd			Even	Odd	
Backplane J5	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
This connector is all User Defined pins.									

Payload Slot Profile SLT6-PAY-4F1Q2U2T-10.2.1— P6 & J6

Plug-In	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P6			Even	Odd			Even	Odd	
Backplane J6	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
This connector is all User Defined pins.									

6U OpenVPX BACKPLANE

BKP6-CEN10-11.2.6-3

VITA 46
VITA 65



Switch Slot Profile SLT6-SWH-16U20F-10.4.2— P1& J1

Plug-In		Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P1				Even	Odd			Even	Odd	
Bplane J1		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	CPlane Inter-SW UTPs Ports 1-4	GDiscrete1	GND	GND-J1	CSutp01-T-	CSutp01-T+	GND	GND-J1	CSutp01-R-	CSutp01-R+
2		GND	CSutp02-T-	CSutp02-T+	GND-J1	GND	CSutp02-R-	CSutp02-R+	GND-J1	GND
3		P1-VBAT	GND	GND-J1	CSutp03-T-	CSutp03-T+	GND	GND-J1	CSutp03-R-	CSutp03-R+
4		GND	CSutp04-T-	CSutp04-T+	GND-J1	GND	CSutp04-R-	CSutp04-R+	GND-J1	GND
5	Control Plane Payload UTPs Ports 1-12	SYS_CON*	GND	GND-J1	CPutp01-T-	CPutp01-T+	GND	GND-J1	CPutp01-R-	CPutp01-R+
6		GND	CPutp02-T-	CPutp02-T+	GND-J1	GND	CPutp02-R-	CPutp02-R+	GND-J1	GND
7		Reserved	GND	GND-J1	CPutp03-T-	CPutp03-T+	GND	GND-J1	CPutp03-R-	CPutp03-R+
8		GND	CPutp04-T-	CPutp04-T+	GND-J1	GND	CPutp04-R-	CPutp04-R+	GND-J1	GND
9		UD	GND	GND-J1	CPutp05-T-	CPutp05-T+	GND	GND-J1	CPutp05-R-	CPutp05-R+
10		GND	CPutp06-T-	CPutp06-T+	GND-J1	GND	CPutp06-R-	CPutp06-R+	GND-J1	GND
11		UD	GND	GND-J1	CPutp07-T-	CPutp07-T+	GND	GND-J1	CPutp07-R-	CPutp07-R+
12		GND	CPutp08-T-	CPutp08-T+	GND-J1	GND	CPutp08-R-	CPutp08-R+	GND-J1	GND
13		UD	GND	GND-J1	CPutp09-T-	CPutp09-T+	GND	GND-J1	CPutp09-R-	CPutp09-R+
14		GND	CPutp10-T-	CPutp10-T+	GND-J1	GND	CPutp10-R-	CPutp10-R+	GND-J1	GND
15		Maskable Reset*	GND	GND-J1	CPutp11-T-	CPutp11-T+	GND	GND-J1	CPutp11-R-	CPutp11-R+
16		GND	CPutp12-T-	CPutp12-T+	GND-J1	GND	CPutp12-R-	CPutp12-R+	GND-J1	GND

Switch Slot Profile SLT6-SWH-16U20F-10.4.2— P2 & J2

Plug-In		Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P2				Even	Odd			Even	Odd	
Bplane J2		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane FP Port 16	UD	GND	GND-J2	DP16-T0-	DP16-T0+	GND	GND-J2	DP16-R0-	DP16-R0+
2		GND	DP16-T1-	DP16-T1+	GND-J2	GND	DP16-R1-	DP16-R1+	GND-J2	GND
3		UD	GND	GND-J2	DP16-T2-	DP16-T2+	GND	GND-J2	DP16-R2-	DP16-R2+
4		GND	DP16-T3-	DP16-T3+	GND-J2	GND	DP16-R3-	DP16-R3+	GND-J2	GND
5	Data Plane FP Port 15	UD	GND	GND-J2	DP15-T0-	DP15-T0+	GND	GND-J2	DP15-R0-	DP15-R0+
6		GND	DP15-T1-	DP15-T1+	GND-J2	GND	DP15-R1-	DP15-R1+	GND-J2	GND
7		UD	GND	GND-J2	DP15-T2-	DP15-T2+	GND	GND-J2	DP15-R2-	DP15-R2+
8		GND	DP15-T3-	DP15-T3+	GND-J2	GND	DP15-R3-	DP15-R3+	GND-J2	GND
9	Data Plane FP Port 14	UD	GND	GND-J2	DP14-T0-	DP14-T0+	GND	GND-J2	DP14-R0-	DP14-R0+
10		GND	DP14-T1-	DP14-T1+	GND-J2	GND	DP14-R1-	DP14-R1+	GND-J2	GND
11		UD	GND	GND-J2	DP14-T2-	DP14-T2+	GND	GND-J2	DP14-R2-	DP14-R2+
12		GND	DP14-T3-	DP14-T3+	GND-J2	GND	DP14-R3-	DP14-R3+	GND-J2	GND
13	Data Plane FP Port 13	UD	GND	GND-J2	DP13-T0-	DP13-T0+	GND	GND-J2	DP13-R0-	DP13-R0+
14		GND	DP13-T1-	DP13-T1+	GND-J2	GND	DP13-R1-	DP13-R1+	GND-J2	GND
15		UD	GND	GND-J2	DP13-T2-	DP13-T2+	GND	GND-J2	DP13-R2-	DP13-R2+
16		GND	DP13-T3-	DP13-T3+	GND-J2	GND	DP13-R3-	DP13-R3+	GND-J2	GND

6U OpenVPX BACKPLANE

BKP6-CEN10-11.2.6-3

VITA 46
VITA 65



Switch Slot Profile SLT6-SWH-16U20F-10.4.2— P3 & J3

Plug-In	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P3			Even	Odd			Even	Odd	
Bplane J3	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J3	DP 12-T0-	DP 12-T0+	GND	GND-J3	DP 12-R0-	DP 12-R0+
2	GND	DP 12-T1-	DP 12-T1+	GND-J3	GND	DP 12-R1-	DP 12-R1+	GND-J3	GND
3	UD	GND	GND-J3	DP 12-T2-	DP 12-T2+	GND	GND-J3	DP 12-R2-	DP 12-R2+
4	GND	DP 12-T3-	DP 12-T3+	GND-J3	GND	DP 12-R3-	DP 12-R3+	GND-J3	GND
5	UD	GND	GND-J3	DP 11-T0-	DP 11-T0+	GND	GND-J3	DP 11-R0-	DP 11-R0+
6	GND	DP 11-T1-	DP 11-T1+	GND-J3	GND	DP 11-R1-	DP 11-R1+	GND-J3	GND
7	UD	GND	GND-J3	DP 11-T2-	DP 11-T2+	GND	GND-J3	DP 11-R2-	DP 11-R2+
8	GND	DP 11-T3-	DP 11-T3+	GND-J3	GND	DP 11-R3-	DP 11-R3+	GND-J3	GND
9	UD	GND	GND-J3	DP 10-T0-	DP 10-T0+	GND	GND-J3	DP 10-R0-	DP 10-R0+
10	GND	DP 10-T1-	DP 10-T1+	GND-J3	GND	DP 10-R1-	DP 10-R1+	GND-J3	GND
11	UD	GND	GND-J3	DP 10-T2-	DP 10-T2+	GND	GND-J3	DP 10-R2-	DP 10-R2+
12	GND	DP 10-T3-	DP 10-T3+	GND-J3	GND	DP 10-R3-	DP 10-R3+	GND-J3	GND
13	UD	GND	GND-J3	DP 09-T0-	DP 09-T0+	GND	GND-J3	DP 09-R0-	DP 09-R0+
14	GND	DP 09-T1-	DP 09-T1+	GND-J3	GND	DP 09-R1-	DP 09-R1+	GND-J3	GND
15	UD	GND	GND-J3	DP 09-T2-	DP 09-T2+	GND	GND-J3	DP 09-R2-	DP 09-R2+
16	GND	DP 09-T3-	DP 09-T3+	GND-J3	GND	DP 09-R3-	DP 09-R3+	GND-J3	GND

Switch Slot Profile SLT6-SWH-16U20F-10.4.2— P4 & J4

Plug-in	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P4									
Bplane J4	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J4	DP 08-T0-	DP 08-T0+	GND	GND-J4	DP 08-R0-	DP 08-R0+
2	GND	DP 08-T1-	DP 08-T1+	GND-J4	GND	DP 08-R1-	DP 08-R1+	GND-J4	GND
3	UD	GND	GND-J4	DP 08-T2-	DP 08-T2+	GND	GND-J4	DP 08-R2-	DP 08-R2+
4	GND	DP 08-T3-	DP 08-T3+	GND-J4	GND	DP 08-R3-	DP 08-R3+	GND-J4	GND
5	UD	GND	GND-J4	DP 07-T0-	DP 07-T0+	GND	GND-J4	DP 07-R0-	DP 07-R0+
6	GND	DP 07-T1-	DP 07-T1+	GND-J4	GND	DP 07-R1-	DP 07-R1+	GND-J4	GND
7	UD	GND	GND-J4	DP 07-T2-	DP 07-T2+	GND	GND-J4	DP 07-R2-	DP 07-R2+
8	GND	DP 07-T3-	DP 07-T3+	GND-J4	GND	DP 07-R3-	DP 07-R3+	GND-J4	GND
9	UD	GND	GND-J4	DP 06-T0-	DP 06-T0+	GND	GND-J4	DP 06-R0-	DP 06-R0+
10	GND	DP 06-T1-	DP 06-T1+	GND-J4	GND	DP 06-R1-	DP 06-R1+	GND-J4	GND
11	UD	GND	GND-J4	DP 06-T2-	DP 06-T2+	GND	GND-J4	DP 06-R2-	DP 06-R2+
12	GND	DP 06-T3-	DP 06-T3+	GND-J4	GND	DP 06-R3-	DP 06-R3+	GND-J4	GND
13	UD	GND	GND-J4	DP 05-T0-	DP 05-T0+	GND	GND-J4	DP 05-R0-	DP 05-R0+
14	GND	DP 05-T1-	DP 05-T1+	GND-J4	GND	DP 05-R1-	DP 05-R1+	GND-J4	GND
15	UD	GND	GND-J4	DP 05-T2-	DP 05-T2+	GND	GND-J4	DP 05-R2-	DP 05-R2+
16	GND	DP 05-T3-	DP 05-T3+	GND-J4	GND	DP 05-R3-	DP 05-R3+	GND-J4	GND

6U OpenVPX BACKPLANE

BKP6-CEN10-11.2.6-3

VITA 46
VITA 65



Switch Slot Profile SLT6-SWH-16U20F-10.4.2— P5 & J5

Plug-In		Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P5										
Bplane J5		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane Pay-load FP Port 4	UD	GND	GND-J5	DP04-T0-	DP04-T0+	GND	GND-J5	DP04-R0-	DP04-R0+
2		GND	DP04-T1-	DP04-T1+	GND-J5	GND	DP04-R1-	DP04-R1+	GND-J5	GND
3		UD	GND	GND-J5	DP04-T2-	DP04-T2+	GND	GND-J5	DP04-R2-	DP04-R2+
4		GND	DP04-T3-	DP04-T3+	GND-J5	GND	DP04-R3-	DP04-R3+	GND-J5	GND
5	Data Plane Pay-load FP Port 3	UD	GND	GND-J5	DP03-T0-	DP03-T0+	GND	GND-J5	DP03-R0-	DP03-R0+
6		GND	DP03-T1-	DP03-T1+	GND-J5	GND	DP03-R1-	DP03-R1+	GND-J5	GND
7		UD	GND	GND-J5	DP03-T2-	DP03-T2+	GND	GND-J5	DP03-R2-	DP03-R2+
8		GND	DP03-T3-	DP03-T3+	GND-J5	GND	DP03-R3-	DP03-R3+	GND-J5	GND
9	Data Plane Pay-load FP Port 2	UD	GND	GND-J5	DP02-T0-	DP02-T0+	GND	GND-J5	DP02-R0-	DP02-R0+
10		GND	DP02-T1-	DP02-T1+	GND-J5	GND	DP02-R1-	DP02-R1+	GND-J5	GND
11		UD	GND	GND-J5	DP02-T2-	DP02-T2+	GND	GND-J5	DP02-R2-	DP02-R2+
12		GND	DP02-T3-	DP02-T3+	GND-J5	GND	DP02-R3-	DP02-R3+	GND-J5	GND
13	Data Plane Pay-load FP Port 1	UD	GND	GND-J5	DP01-T0-	DP01-T0+	GND	GND-J5	DP01-R0-	DP01-R0+
14		GND	DP01-T1-	DP01-T1+	GND-J5	GND	DP01-R1-	DP01-R1+	GND-J5	GND
15		UD	GND	GND-J5	DP01-T2-	DP01-T2+	GND	GND-J5	DP01-R2-	DP01-R2+
16		GND	DP01-T3-	DP01-T3+	GND-J5	GND	DP01-R3-	DP01-R3+	GND-J5	GND

Switch Slot Profile SLT6-SWH-16U20F-10.4.2— P6 & J6

Plug-In		Row G	Row F	Row E		Row D	Row C	Row B		Row A
Module P6				Even	Odd			Even	Odd	
Bplane J6		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane Inter-SW FP Port 4	UD	GND	GND-J6	DS04-T0-	DS04-T0+	GND	GND-J6	DS04-R0-	DS04-R0+
2		GND	DS04-T1-	DS04-T1+	GND-J6	GND	DS04-R1-	DS04-R1+	GND-J6	GND
3		UD	GND	GND-J6	DS04-T2-	DS04-T2+	GND	GND-J6	DS04-R2-	DS04-R2+
4		GND	DS04-T3-	DS04-T3+	GND-J6	GND	DS04-R3-	DS04-R3+	GND-J6	GND
5	Data Plane Inter-SW FP Port 3	UD	GND	GND-J6	DS03-T0-	DS03-T0+	GND	GND-J6	DS03-R0-	DS03-R0+
6		GND	DS03-T1-	DS03-T1+	GND-J6	GND	DS03-R1-	DS03-R1+	GND-J6	GND
7		UD	GND	GND-J6	DS03-T2-	DS03-T2+	GND	GND-J6	DS03-R2-	DS03-R2+
8		GND	DS03-T3-	DS03-T3+	GND-J6	GND	DS03-R3-	DS03-R3+	GND-J6	GND
9	Data Plane Inter-SW FP Port 2	UD	GND	GND-J6	DS02-T0-	DS02-T0+	GND	GND-J6	DS02-R0-	DS02-R0+
10		GND	DS02-T1-	DS02-T1+	GND-J6	GND	DS02-R1-	DS02-R1+	GND-J6	GND
11		UD	GND	GND-J6	DS02-T2-	DS02-T2+	GND	GND-J6	DS02-R2-	DS02-R2+
12		GND	DS02-T3-	DS02-T3+	GND-J6	GND	DS02-R3-	DS02-R3+	GND-J6	GND
13	Data Plane Inter-SW FP Port 1	UD	GND	GND-J6	DS01-T0-	DS01-T0+	GND	GND-J6	DS01-R0-	DS01-R0+
14		GND	DS01-T1-	DS01-T1+	GND-J6	GND	DS01-R1-	DS01-R1+	GND-J6	GND
15		UD	GND	GND-J6	DS01-T2-	DS01-T2+	GND	GND-J6	DS01-R2-	DS01-R2+
16		GND	DS01-T3-	DS01-T3+	GND-J6	GND	DS01-R3-	DS01-R3+	GND-J6	GND

6U OpenVPX BACKPLANE

BKP6-CEN10-11.2.6-3

VITA 46
VITA 65



Pin Assignment VPX J0 (Utility Connector)

PIN NUMBER	ROW I VPX	ROW H VPX	ROW G VPX	ROW F VPX	ROW E VPX	ROW D VPX	ROW C VPX	ROW B VPX	ROW A VPX
1	VS1	VS1	VS1	VS1	None	VS2	VS2	VS2	VS2
2	VS1	VS1	VS1	VS1	None	VS2	VS2	VS2	VS2
3	VS3	VS3	VS3	VS3	None	VS3	VS3	VS3	VS3
4	GND	SM2	SM3	GND	-12V_AUX	GND	SYSRESET*	NVMRO	GND
5	GND	GAP*	GA4*	GND	3.3V_AUX	GND	SM0	SM1	GND
6	GND	GA3*	GA2*	GND	+12V_AUX	GND	GA1*	GA0*	GND
7	TCK	GND	GND	TDO	TDI	GND	GND	TMS	TRST*
8	GND	REF_CLK-	REF_CLK+	GND	GND	AUX_CLK-	AUX_CLK+	GND	GND

VS1=12V, VS2=3.3V, VS3=5V

3) Current Capability:

+12V	240 A
+3.3V	240 A
+5V	220 A
-12V AUX	8 A
+12V AUX	8 A
+3.3V AUX	8 A

Consider: Total power consumption for the main voltages +12V, 5V and 3.3V is max. 36A/ Slot according VITA 46.0

4) JTAG (connector XJT1)

For test and programming a JTAG connector (6-poles) is implemented (XJT1).

XJT1	Signal
1	GND
2	TCK
3	TMS
4	TRST-
5	TDI
6	TDO

5) SYSCON

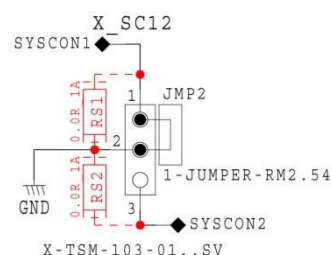
By setting the signal Syscon to GND the system slot is defined. In general the system slot is slot 1.

We offer 2 options for setting:

- Jumper (standard)
- 0 Ohm Resistor for rugged applications

X_SC12

1	SYSCON1
2	GND
3	SYSCON2



6U OpenVPX BACKPLANE

BKP6-CEN10-11.2.6-3

VITA 46
VITA 65



6) I2C connector

There are 2 connectors for system-management I2CA and I2CB.

For customer specific board assembly Zero-Ohm resistors available.

Usable voltages for I2C are 3.3V-AUX

I2CA

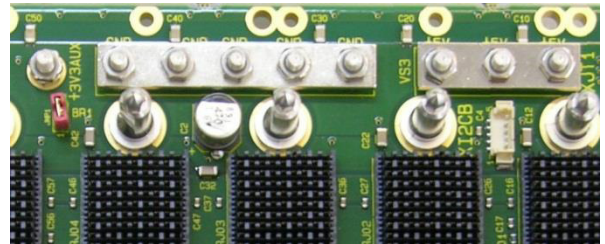
1	I2CA_SCL
2	GND
3	I2CA_SDA
4	I2CA_PWR
5	NC

I2CB

1	I2CB_SCL
2	GND
3	I2CB_SDA
4	I2CB_PWR
5	NC

7) Power Studs M3

The main operating voltages (+12V, +3.3V, +5V) and GND are supplied with M3 screw terminals. The auxiliary operating voltages are supplied via M3 screw terminals. Optimal daughter board supply and trouble-free operation are ensured by the arrangement of the feed modules on the backplane.

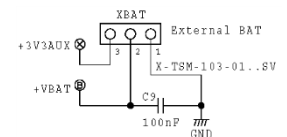


8) XBAT

Normally a battery voltage with approximately 3V is available at Pin VBAT of connector VPX-J1. The voltage is externally accessible with connector XBAT, Pin2 or internally using 3.3V_AUX by setting a Jumper between Pin2 and Pin3.

VBAT X5

1	GND
2	+VBAT
3	+3.3V_AUX

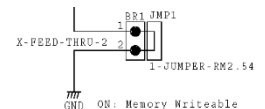


9) NVMRO

If Jumper BR1 is closed NVRMO is set to memory writeable.

BR1

1	NVMRO
2	GND



Germany

Hartmann Electronic GmbH
Phone: +49 711 13 98 90
Fax: +49 711 8 66 11 91
vertrieb.he@kontron.com
www.hartmann-electronic.com

USA

Kontron
Fabian Hemmann
Phone: +1 937-324-2420
Mobile: +1 937 346 7878
fabian.hemmann@us.kontron.com
www.hartmann-electronic.com

France

Kontron Modular Computers S.A.S.
Serge Pichat
Phone: +33 (0)9 66 44 03 15
Mobile: +33 (0)6 82 62 16 00
Serge.pichat@kontron.com
www.hartmann-electronic.com

India

Hartmann Electronic GmbH
Vivek Deshpande
Phone: +1 91 20 66 74 51 23
Vivek.Deshpande@kontron.com
www.hartmann-electronic.com