

6U Open VPX BACKPLANE
BKP6-CEN05-11.2.5-4

VITA 46
VITA 65



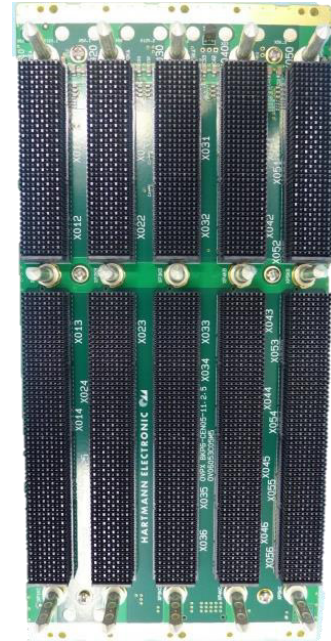
Key figures:

- Compliant to VITA 46.0 baseline specification
- Compliant to VITA 65 Open VPX 
- 5 Slots VPX, 4 Payload Slots , 1 Switch Slots
- Dual Star X4 configuration for PCI Express
- M3 studs for main voltage power entry
- PCB size 123.9 mm x 261.9mm x 5.4 mm
- 5 HP from slot to slot (25.40 mm) 1"
- Flexible keying and alignment mechanism
- System Management Interface on the backplane (IPMB)
- with geographical address pins
- Reference clock
- Non-Volatile Memory Read Only signal set by Jumper BR1
- Battery backup option setting by Jumper XBAT for Vbat external or connected to 3.3 AUX.
- Power Consumption:
VS1:VS2:VS3 = 60A : 60A : 110A
- System Reset
- Operating temperature: -40° - +85°C
- Storage temperature: -55°C - +85°C
- Flammability rating: UL94-V0
- RoHS compliant

Order number:

B196411061 with rear IO on all slots

Front side



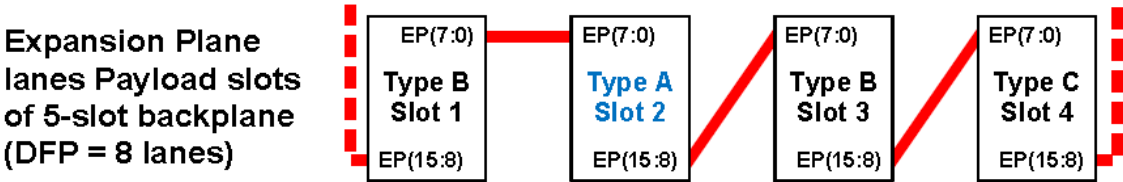
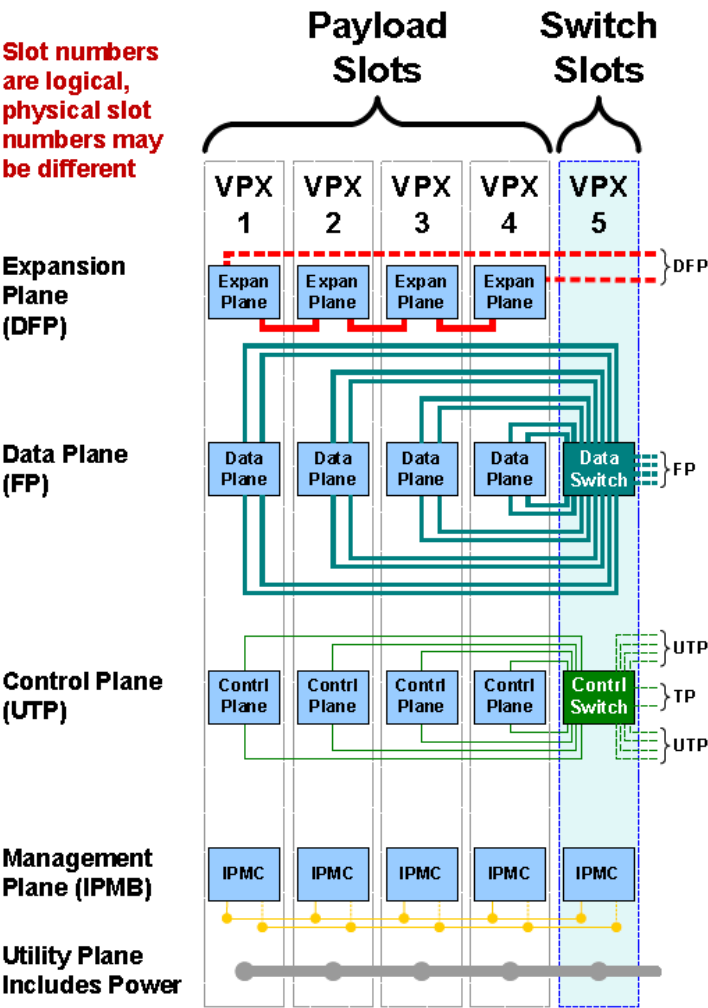
Rear side



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1) Topology: BKP6-CEN05-11.2.5-4

Slot Profiles		Chanel Gbaud Rate		
Payload	Switch	Control Plane	Data Plane	Exp. Plane
SLT6-PAY- 4F1Q2U2T-10.2.1	SLT6-SWH-16U20F-10.4.2	1.25	10.3125	8.0



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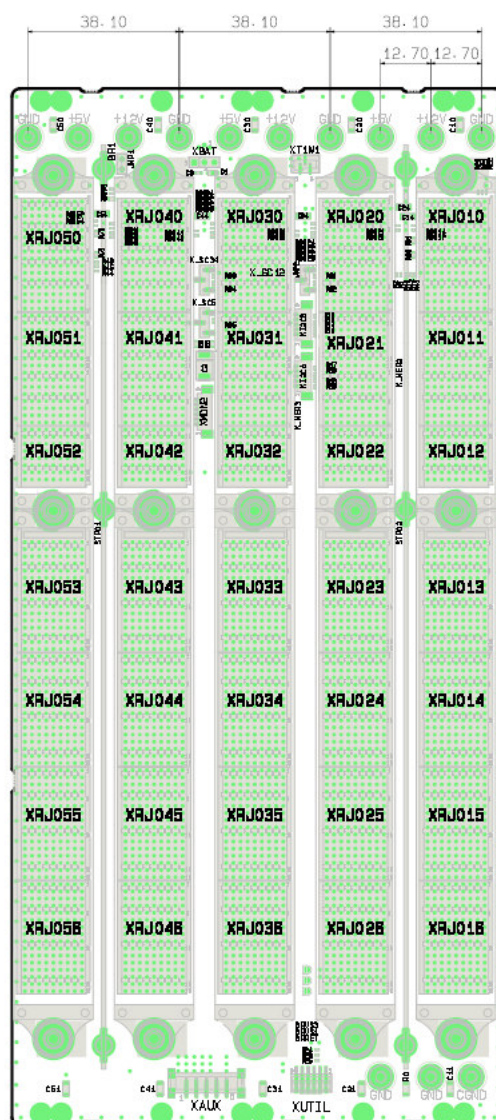


2) Drawings

Front side



Rear side



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3) Pin Assignment

Utility Plane Signals on P0

	Row G	Row F	Row E	Row D	Row C	Row B	Row A
1	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2
2	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2
3	Vs3	Vs3	Vs3	No Pad*	Vs3	Vs3	Vs3
4	SM2	SM3	GND	-12V_Aux	GND	SYSRESET*	NVMRO
5	GAP*	GA4*	GND	3.3V_Aux	GND	SM0	SM1
6	GA3*	GA2*	GND	+12V_Aux	GND	GA1*	GA0*
7	TCK	GND	TDO	TDI	GND	TMS	TRST*
8	GND	REF_CLK-	REF_CLK+	GND	AUX_CLK-	AUX_CLK+	GND

Utility Plane Signals on J0

	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Vs1	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2	Vs2
2	Vs1	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2	Vs2
3	Vs3	Vs3	Vs3	Vs3	No Pad*	Vs3	Vs3	Vs3	Vs3
4	GND	SM2	SM3	GND	-12V_Aux	GND	SYSRESET*	NVMRO	GND
5	GND	GAP*	GA4*	GND	3.3V_Aux	GND	SM0	SM1	GND
6	GND	GA3*	GA2*	GND	+12V_Aux	GND	GA1*	GA0*	GND
7	TCK	GND	GND	TDO	TDI	GND	GND	TMS	TRST*
8	GND	REF_CLK-	REF_CLK+	GND	GND	AUX_CLK-	AUX_CLK+	GND	GND

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Payload Slot Profile SLT3-PAY-4F1Q2U2T-10.2.1 — P1 & J1

Plug-In Module P1	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J1	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	GDiscrete1	GND	GND-J1	DP01-T0-	DP01-T0+	GND	GND-J1	DP01-R0-	DP01-R0+
2	GND	DP01-T1-	DP01-T1+	GND-J1	GND	DP01-R1-	DP01-R1+	GND-J1	GND
3	P1-VBAT	GND	GND-J1	DP01-T2-	DP01-T2+	GND	GND-J1	DP01-R2-	DP01-R2+
4	GND	DP01-T3-	DP01-T3+	GND-J1	GND	DP01-R3-	DP01-R3+	GND-J1	GND
5	SYS_CON*	GND	GND-J1	DP02-T0-	DP02-T0+	GND	GND-J1	DP02-R0-	DP02-R0+
6	GND	DP02-T1-	DP02-T1+	GND-J1	GND	DP02-R1-	DP02-R1+	GND-J1	GND
7	Reserved	GND	GND-J1	DP02-T2-	DP02-T2+	GND	GND-J1	DP02-R2-	DP02-R2+
8	GND	DP02-T3-	DP02-T3+	GND-J1	GND	DP02-R3-	DP02-R3+	GND-J1	GND
9	UD	GND	GND-J1	DP03-T0-	DP03-T0+	GND	GND-J1	DP03-R0-	DP03-R0+
10	GND	DP03-T1-	DP03-T1+	GND-J1	GND	DP03-R1-	DP03-R1+	GND-J1	GND
11	UD	GND	GND-J1	DP03-T2-	DP03-T2+	GND	GND-J1	DP03-R2-	DP03-R2+
12	GND	DP03-T3-	DP03-T3+	GND-J1	GND	DP03-R3-	DP03-R3+	GND-J1	GND
13	UD	GND	GND-J1	DP04-T0-	DP04-T0+	GND	GND-J1	DP04-R0-	DP04-R0+
14	GND	DP04-T1-	DP04-T1+	GND-J1	GND	DP04-R1-	DP04-R1+	GND-J1	GND
15	Maskable Reset*	GND	GND-J1	DP04-T2-	DP04-T2+	GND	GND-J1	DP04-R2-	DP04-R2+
16	GND	DP04-T3-	DP04-T3+	GND-J1	GND	DP04-R3-	DP04-R3+	GND-J1	GND

Payload Slot Profile SLT6-PAY-4F1Q2U2T-10.2.1 — P2 & J2

Plug-In Module P2	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Backplane J2	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J2	EP00-T-	EP00-T+	GND	GND-J2	EP00-R-	EP00-R+
2	GND	EP01-T-	EP01-T+	GND-J2	GND	EP01-R-	EP01-R+	GND-J2	GND
3	UD	GND	GND-J2	EP02-T-	EP02-T+	GND	GND-J2	EP02-R-	EP02-R+
4	GND	EP03-T-	EP03-T+	GND-J2	GND	EP03-R-	EP03-R+	GND-J2	GND
5	UD	GND	GND-J2	EP04-T-	EP04-T+	GND	GND-J2	EP04-R-	EP04-R+
6	GND	EP05-T-	EP05-T+	GND-J2	GND	EP05-R-	EP05-R+	GND-J2	GND
7	UD	GND	GND-J2	EP06-T-	EP06-T+	GND	GND-J2	EP06-R-	EP06-R+
8	GND	EP07-T-	EP07-T+	GND-J2	GND	EP07-R-	EP07-R+	GND-J2	GND
9	UD	GND	GND-J2	EP08-T-	EP08-T+	GND	GND-J2	EP08-R-	EP08-R+
10	GND	EP09-T-	EP09-T+	GND-J2	GND	EP09-R-	EP09-R+	GND-J2	GND
11	UD	GND	GND-J2	EP10-T-	EP10-T+	GND	GND-J2	EP10-R-	EP10-R+
12	GND	EP11-T-	EP11-T+	GND-J2	GND	EP11-R-	EP11-R+	GND-J2	GND
13	UD	GND	GND-J2	EP12-T-	EP12-T+	GND	GND-J2	EP12-R-	EP12-R+
14	GND	EP13-T-	EP13-T+	GND-J2	GND	EP13-R-	EP13-R+	GND-J2	GND
15	UD	GND	GND-J2	EP14-T-	EP14-T+	GND	GND-J2	EP14-R-	EP14-R+
16	GND	EP15-T-	EP15-T+	GND-J2	GND	EP15-R-	EP15-R+	GND-J2	GND

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Universal Ground Pattern User Defined pins for P3 acc. Table 6.3.3.1-5

Backplane J2	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	UD	UD	UD	UD	GND	UD	UD	UD
2	GND	UD	UD	GND	UD	UD	UD	UD	GND
3	UD	UD	UD	UD	UD	GND	UD	UD	UD
4	GND	UD	UD	GND	UD	UD	UD	UD	GND
5	UD	UD	UD	UD	UD	GND	UD	UD	UD
6	GND	UD	UD	GND	UD	UD	UD	UD	GND
7	UD	UD	UD	UD	UD	GND	UD	UD	UD
8	GND	UD	UD	GND	UD	UD	UD	UD	GND
9	UD	UD	UD	UD	UD	GND	UD	UD	UD
10	GND	UD	UD	GND	UD	UD	UD	UD	GND
11	UD	UD	UD	UD	UD	GND	UD	UD	UD
12	GND	UD	UD	GND	UD	UD	UD	UD	GND
13	UD	UD	UD	UD	UD	GND	UD	UD	UD
14	GND	UD	UD	GND	UD	UD	UD	UD	GND
15	UD	UD	UD	UD	UD	GND	UD	UD	UD
16	GND	UD	UD	GND	UD	UD	UD	UD	GND

Payload Slot Profile SLT6-PAY-4F1Q2U2T-10.2.1 — P4 & J4

Plug-In Mod P4	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J4	Row i	Row h	Even	Odd	Row e	Row d	Even	Odd	Row a
1	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
2	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
3	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
4	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
5	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
6	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
7	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
8	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
9	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
10	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
11	UD	GND	GND-J4	CPTp02-T-	CPTp02-T+	GND	GND-J4	CPTp02-R-	CPTp02-R+
12	GND	CPTp01-T-	CPTp01-T+	GND-J4	GND	CPTp01-R-	CPTp01-R+	GND-J4	GND
13	UD	GND	GND-J4	CPTp02-DB-	CPTp02-DB+	GND	GND-J4	CPTp02-DA-	CPTp02-DA+
14	GND	CPTp02-DD-	CPTp02-DD+	GND-J4	GND	CPTp02-DC-	CPTp02-DC+	GND-J4	GND
15	UD	GND	GND-J4	CPTp01-DB-	CPTp01-DB+	GND	GND-J4	CPTp01-DA-	CPTp01-DA+
16	GND	CPTp01-DD-	CPTp01-DD+	GND-J4	GND	CPTp01-DC-	CPTp01-DC+	GND-J4	GND

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Universal Ground Pattern User Defined pins for P5 acc. Table 6.3.3.1-5

Backplane J2	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	UD	UD	UD	UD	GND	UD	UD	UD
2	GND	UD	UD	GND	UD	UD	UD	UD	GND
3	UD	UD	UD	UD	UD	GND	UD	UD	UD
4	GND	UD	UD	GND	UD	UD	UD	UD	GND
5	UD	UD	UD	UD	UD	GND	UD	UD	UD
6	GND	UD	UD	GND	UD	UD	UD	UD	GND
7	UD	UD	UD	UD	UD	GND	UD	UD	UD
8	GND	UD	UD	GND	UD	UD	UD	UD	GND
9	UD	UD	UD	UD	UD	GND	UD	UD	UD
10	GND	UD	UD	GND	UD	UD	UD	UD	GND
11	UD	UD	UD	UD	UD	GND	UD	UD	UD
12	GND	UD	UD	GND	UD	UD	UD	UD	GND
13	UD	UD	UD	UD	UD	GND	UD	UD	UD
14	GND	UD	UD	GND	UD	UD	UD	UD	GND
15	UD	UD	UD	UD	UD	GND	UD	UD	UD
16	GND	UD	UD	GND	UD	UD	UD	UD	GND

Universal Ground Pattern User Defined pins for P6 acc. Table 6.3.3.1-5

Backplane J2	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	UD	UD	UD	UD	GND	UD	UD	UD
2	GND	UD	UD	GND	UD	UD	UD	UD	GND
3	UD	UD	UD	UD	UD	GND	UD	UD	UD
4	GND	UD	UD	GND	UD	UD	UD	UD	GND
5	UD	UD	UD	UD	UD	GND	UD	UD	UD
6	GND	UD	UD	GND	UD	UD	UD	UD	GND
7	UD	UD	UD	UD	UD	GND	UD	UD	UD
8	GND	UD	UD	GND	UD	UD	UD	UD	GND
9	UD	UD	UD	UD	UD	GND	UD	UD	UD
10	GND	UD	UD	GND	UD	UD	UD	UD	GND
11	UD	UD	UD	UD	UD	GND	UD	UD	UD
12	GND	UD	UD	GND	UD	UD	UD	UD	GND
13	UD	UD	UD	UD	UD	GND	UD	UD	UD
14	GND	UD	UD	GND	UD	UD	UD	UD	GND
15	UD	UD	UD	UD	UD	GND	UD	UD	UD
16	GND	UD	UD	GND	UD	UD	UD	UD	GND

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Switch Slot Profile SLT6-SWH-16U20F-10.4.2 — P1 & J1

Plug-In Module P1	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J1	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	GDiscrete1	GND	GND-J1	CSutp01-T-	CSutp01-T+	GND	GND-J1	CSutp01-R-	CSutp01-R+
2	GND	CSutp02-T-	CSutp02-T+	GND-J1	GND	CSutp02-R-	CSutp02-R+	GND-J1	GND
3	P1-VBAT	GND	GND-J1	CSutp03-T-	CSutp03-T+	GND	GND-J1	CSutp03-R-	CSutp03-R+
4	GND	CSutp04-T-	CSutp04-T+	GND-J1	GND	CSutp04-R-	CSutp04-R+	GND-J1	GND
5	SYS_CON*	GND	GND-J1	CPutp01-T-	CPutp01-T+	GND	GND-J1	CPutp01-R-	CPutp01-R+
6	GND	CPutp02-T-	CPutp02-T+	GND-J1	GND	CPutp02-R-	CPutp02-R+	GND-J1	GND
7	Reserved	GND	GND-J1	CPutp03-T-	CPutp03-T+	GND	GND-J1	CPutp03-R-	CPutp03-R+
8	GND	CPutp04-T-	CPutp04-T+	GND-J1	GND	CPutp04-R-	CPutp04-R+	GND-J1	GND
9	UD	GND	GND-J1	CPutp05-T-	CPutp05-T+	GND	GND-J1	CPutp05-R-	CPutp05-R+
10	GND	CPutp06-T-	CPutp06-T+	GND-J1	GND	CPutp06-R-	CPutp06-R+	GND-J1	GND
11	UD	GND	GND-J1	CPutp07-T-	CPutp07-T+	GND	GND-J1	CPutp07-R-	CPutp07-R+
12	GND	CPutp08-T-	CPutp08-T+	GND-J1	GND	CPutp08-R-	CPutp08-R+	GND-J1	GND
13	UD	GND	GND-J1	CPutp09-T-	CPutp09-T+	GND	GND-J1	CPutp09-R-	CPutp09-R+
14	GND	CPutp10-T-	CPutp10-T+	GND-J1	GND	CPutp10-R-	CPutp10-R+	GND-J1	GND
15	Maskable Reset*	GND	GND-J1	CPutp11-T-	CPutp11-T+	GND	GND-J1	CPutp11-R-	CPutp11-R+
16	GND	CPutp12-T-	CPutp12-T+	GND-J1	GND	CPutp12-R-	CPutp12-R+	GND-J1	GND

Switch Slot Profile SLT6-SWH-16U20F-10.4.2 — P2 & J2

Plug-In Module P2	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J2	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J2	DP16-T0-	DP16-T0+	GND	GND-J2	DP16-R0-	DP16-R0+
2	GND	DP16-T1-	DP16-T1+	GND-J2	GND	DP16-R1-	DP16-R1+	GND-J2	GND
3	UD	GND	GND-J2	DP16-T2-	DP16-T2+	GND	GND-J2	DP16-R2-	DP16-R2+
4	GND	DP16-T3-	DP16-T3+	GND-J2	GND	DP16-R3-	DP16-R3+	GND-J2	GND
5	UD	GND	GND-J2	DP15-T0-	DP15-T0+	GND	GND-J2	DP15-R0-	DP15-R0+
6	GND	DP15-T1-	DP15-T1+	GND-J2	GND	DP15-R1-	DP15-R1+	GND-J2	GND
7	UD	GND	GND-J2	DP15-T2-	DP15-T2+	GND	GND-J2	DP15-R2-	DP15-R2+
8	GND	DP15-T3-	DP15-T3+	GND-J2	GND	DP15-R3-	DP15-R3+	GND-J2	GND
9	UD	GND	GND-J2	DP14-T0-	DP14-T0+	GND	GND-J2	DP14-R0-	DP14-R0+
10	GND	DP14-T1-	DP14-T1+	GND-J2	GND	DP14-R1-	DP14-R1+	GND-J2	GND
11	UD	GND	GND-J2	DP14-T2-	DP14-T2+	GND	GND-J2	DP14-R2-	DP14-R2+
12	GND	DP14-T3-	DP14-T3+	GND-J2	GND	DP14-R3-	DP14-R3+	GND-J2	GND
13	UD	GND	GND-J2	DP13-T0-	DP13-T0+	GND	GND-J2	DP13-R0-	DP13-R0+
14	GND	DP13-T1-	DP13-T1+	GND-J2	GND	DP13-R1-	DP13-R1+	GND-J2	GND
15	UD	GND	GND-J2	DP13-T2-	DP13-T2+	GND	GND-J2	DP13-R2-	DP13-R2+
16	GND	DP13-T3-	DP13-T3+	GND-J2	GND	DP13-R3-	DP13-R3+	GND-J2	GND

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Switch Slot Profile SLT6-SWH-16U20F-10.4.2 — P3 & J3

Plug-In Module	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J3	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J3	DP12-T0-	DP12-T0+	GND	GND-J3	DP12-R0-	DP12-R0+
2	GND	DP12-T1-	DP12-T1+	GND-J3	GND	DP12-R1-	DP12-R1+	GND-J3	GND
3	UD	GND	GND-J3	DP12-T2-	DP12-T2+	GND	GND-J3	DP12-R2-	DP12-R2+
4	GND	DP12-T3-	DP12-T3+	GND-J3	GND	DP12-R3-	DP12-R3+	GND-J3	GND
5	UD	GND	GND-J3	DP11-T0-	DP11-T0+	GND	GND-J3	DP11-R0-	DP11-R0+
6	GND	DP11-T1-	DP11-T1+	GND-J3	GND	DP11-R1-	DP11-R1+	GND-J3	GND
7	UD	GND	GND-J3	DP11-T2-	DP11-T2+	GND	GND-J3	DP11-R2-	DP11-R2+
8	GND	DP11-T3-	DP11-T3+	GND-J3	GND	DP11-R3-	DP11-R3+	GND-J3	GND
9	UD	GND	GND-J3	DP10-T0-	DP10-T0+	GND	GND-J3	DP10-R0-	DP10-R0+
10	GND	DP10-T1-	DP10-T1+	GND-J3	GND	DP10-R1-	DP10-R1+	GND-J3	GND
11	UD	GND	GND-J3	DP10-T2-	DP10-T2+	GND	GND-J3	DP10-R2-	DP10-R2+
12	GND	DP10-T3-	DP10-T3+	GND-J3	GND	DP10-R3-	DP10-R3+	GND-J3	GND
13	UD	GND	GND-J3	DP09-T0-	DP09-T0+	GND	GND-J3	DP09-R0-	DP09-R0+
14	GND	DP09-T1-	DP09-T1+	GND-J3	GND	DP09-R1-	DP09-R1+	GND-J3	GND
15	UD	GND	GND-J3	DP09-T2-	DP09-T2+	GND	GND-J3	DP09-R2-	DP09-R2+
16	GND	DP09-T3-	DP09-T3+	GND-J3	GND	DP09-R3-	DP09-R3+	GND-J3	GND

Switch Slot Profile SLT6-SWH-16U20F-10.4.2 — P4 & J4

Plug-In Module	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J4	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J4	DP08-T0-	DP08-T0+	GND	GND-J4	DP08-R0-	DP08-R0+
2	GND	DP08-T1-	DP08-T1+	GND-J4	GND	DP08-R1-	DP08-R1+	GND-J4	GND
3	UD	GND	GND-J4	DP08-T2-	DP08-T2+	GND	GND-J4	DP08-R2-	DP08-R2+
4	GND	DP08-T3-	DP08-T3+	GND-J4	GND	DP08-R3-	DP08-R3+	GND-J4	GND
5	UD	GND	GND-J4	DP07-T0-	DP07-T0+	GND	GND-J4	DP07-R0-	DP07-R0+
6	GND	DP07-T1-	DP07-T1+	GND-J4	GND	DP07-R1-	DP07-R1+	GND-J4	GND
7	UD	GND	GND-J4	DP07-T2-	DP07-T2+	GND	GND-J4	DP07-R2-	DP07-R2+
8	GND	DP07-T3-	DP07-T3+	GND-J4	GND	DP07-R3-	DP07-R3+	GND-J4	GND
9	UD	GND	GND-J4	DP06-T0-	DP06-T0+	GND	GND-J4	DP06-R0-	DP06-R0+
10	GND	DP06-T1-	DP06-T1+	GND-J4	GND	DP06-R1-	DP06-R1+	GND-J4	GND
11	UD	GND	GND-J4	DP06-T2-	DP06-T2+	GND	GND-J4	DP06-R2-	DP06-R2+
12	GND	DP06-T3-	DP06-T3+	GND-J4	GND	DP06-R3-	DP06-R3+	GND-J4	GND
13	UD	GND	GND-J4	DP05-T0-	DP05-T0+	GND	GND-J4	DP05-R0-	DP05-R0+
14	GND	DP05-T1-	DP05-T1+	GND-J4	GND	DP05-R1-	DP05-R1+	GND-J4	GND
15	UD	GND	GND-J4	DP05-T2-	DP05-T2+	GND	GND-J4	DP05-R2-	DP05-R2+
16	GND	DP05-T3-	DP05-T3+	GND-J4	GND	DP05-R3-	DP05-R3+	GND-J4	GND

6U Open VPX BACKPLANE
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VITA 65



Switch Slot Profile SLT6-SWH-16U20F-10.4.2 — P5 & J5

Plug-In Module P5	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J5	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J5	DP04-T0-	DP04-T0+	GND	GND-J5	DP04-R0-	DP04-R0+
2	GND	DP04-T1-	DP04-T1+	GND-J5	GND	DP04-R1-	DP04-R1+	GND-J5	GND
3	UD	GND	GND-J5	DP04-T2-	DP04-T2+	GND	GND-J5	DP04-R2-	DP04-R2+
4	GND	DP04-T3-	DP04-T3+	GND-J5	GND	DP04-R3-	DP04-R3+	GND-J5	GND
5	UD	GND	GND-J5	DP03-T0-	DP03-T0+	GND	GND-J5	DP03-R0-	DP03-R0+
6	GND	DP03-T1-	DP03-T1+	GND-J5	GND	DP03-R1-	DP03-R1+	GND-J5	GND
7	UD	GND	GND-J5	DP03-T2-	DP03-T2+	GND	GND-J5	DP03-R2-	DP03-R2+
8	GND	DP03-T3-	DP03-T3+	GND-J5	GND	DP03-R3-	DP03-R3+	GND-J5	GND
9	UD	GND	GND-J5	DP02-T0-	DP02-T0+	GND	GND-J5	DP02-R0-	DP02-R0+
10	GND	DP02-T1-	DP02-T1+	GND-J5	GND	DP02-R1-	DP02-R1+	GND-J5	GND
11	UD	GND	GND-J5	DP02-T2-	DP02-T2+	GND	GND-J5	DP02-R2-	DP02-R2+
12	GND	DP02-T3-	DP02-T3+	GND-J5	GND	DP02-R3-	DP02-R3+	GND-J5	GND
13	UD	GND	GND-J5	DP01-T0-	DP01-T0+	GND	GND-J5	DP01-R0-	DP01-R0+
14	GND	DP01-T1-	DP01-T1+	GND-J5	GND	DP01-R1-	DP01-R1+	GND-J5	GND
15	UD	GND	GND-J5	DP01-T2-	DP01-T2+	GND	GND-J5	DP01-R2-	DP01-R2+
16	GND	DP01-T3-	DP01-T3+	GND-J5	GND	DP01-R3-	DP01-R3+	GND-J5	GND

Switch Slot Profile SLT6-SWH-16U20F-10.4.2 — P6 & J6

Plug-In Module P6	Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J6	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J6	DS04-T0-	DS04-T0+	GND	GND-J6	DS04-R0-	DS04-R0+
2	GND	DS04-T1-	DS04-T1+	GND-J6	GND	DS04-R1-	DS04-R1+	GND-J6	GND
3	UD	GND	GND-J6	DS04-T2-	DS04-T2+	GND	GND-J6	DS04-R2-	DS04-R2+
4	GND	DS04-T3-	DS04-T3+	GND-J6	GND	DS04-R3-	DS04-R3+	GND-J6	GND
5	UD	GND	GND-J6	DS03-T0-	DS03-T0+	GND	GND-J6	DS03-R0-	DS03-R0+
6	GND	DS03-T1-	DS03-T1+	GND-J6	GND	DS03-R1-	DS03-R1+	GND-J6	GND
7	UD	GND	GND-J6	DS03-T2-	DS03-T2+	GND	GND-J6	DS03-R2-	DS03-R2+
8	GND	DS03-T3-	DS03-T3+	GND-J6	GND	DS03-R3-	DS03-R3+	GND-J6	GND
9	UD	GND	GND-J6	DS02-T0-	DS02-T0+	GND	GND-J6	DS02-R0-	DS02-R0+
10	GND	DS02-T1-	DS02-T1+	GND-J6	GND	DS02-R1-	DS02-R1+	GND-J6	GND
11	UD	GND	GND-J6	DS02-T2-	DS02-T2+	GND	GND-J6	DS02-R2-	DS02-R2+
12	GND	DS02-T3-	DS02-T3+	GND-J6	GND	DS02-R3-	DS02-R3+	GND-J6	GND
13	UD	GND	GND-J6	DS01-T0-	DS01-T0+	GND	GND-J6	DS01-R0-	DS01-R0+
14	GND	DS01-T1-	DS01-T1+	GND-J6	GND	DS01-R1-	DS01-R1+	GND-J6	GND
15	UD	GND	GND-J6	DS01-T2-	DS01-T2+	GND	GND-J6	DS01-R2-	DS01-R2+
16	GND	DS01-T3-	DS01-T3+	GND-J6	GND	DS01-R3-	DS01-R3+	GND-J6	GND

6U Open VPX BACKPLANE
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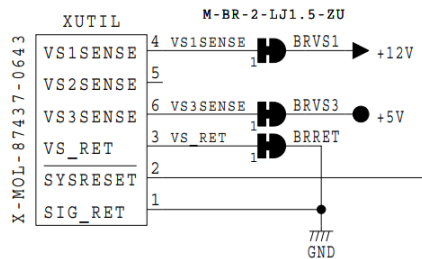
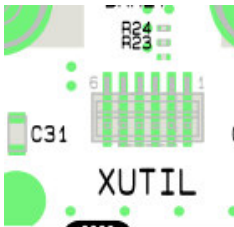
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4) Current Capability:

■ +12V	120 A
■ +5V	110 A
■ -12V AUX	5 A
■ +12V AUX	5 A
■ +3.3V AUX	10 A

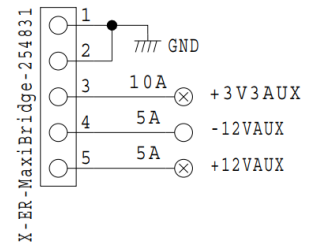
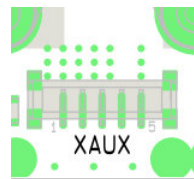
5) UTILITY (Connector XUTIL)



Connector Molex 87437-0643

6) AUXILIARY (connector XAUX)

The auxiliary operating voltages 3.3VAUX and ± 12 VAUX are supplied 5 poles connector Maxibridge 254831.
 Accessory: F006.04880, cable Hartmann 400mm with M3 cable lug (mating connector ERNI 324035+284070)



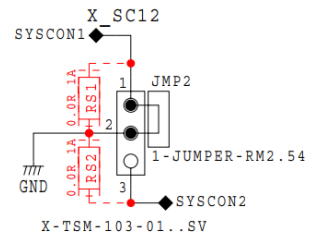
7) SYSCON

By setting the signal Syscon to GND the system slot is defined. In general the system slot is slot 1.

There are additional connectors X_SC34, X_SC5 so as you can select any slot as system slot

X_SC12

1	SYSCON1
2	GND
3	SYSCON2



We offer 2 options for setting:

- Jumper (standard)
- 0 Ohm Resistor for rugged applications

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8) I2C connector

There are 2 connectors for system- management XI2CA and XI2CB.

For customer specific board assembly are Zero-Ohm resistors available.

Usable voltages for I2C are 3.3V-AUX

XI2CA

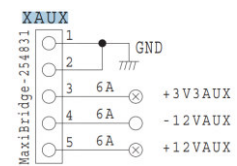
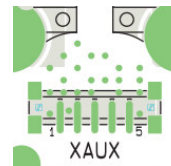
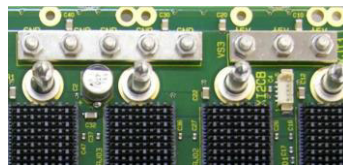
1	IPMB_SCL
2	GND
3	IPMB_SDA
4	IPMB_PWR
5	NC

XI2CB

1	IPMB_SCL
2	GND
3	IPMB_SDA
4	IPMB_PWR
5	NC

9) Power Studs M3

The main operating voltages (+12V, +5V) and GND are supplied with M3 screw terminals. The auxiliary operating voltages are supplied via a 5 poles connector XAUX.



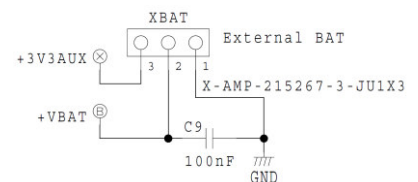
10) XBAT

Normally a battery voltage with approximately 3V is available at Pin VBAT of connector VPX-J1.

The voltage is externally accessible with connector XBAT or internally using 3.3V_AUX by closing Jumper BR2.

XBAT

1	GND
2	+VBAT
2	+3.3V_AUX



11) NVMRO

If Jumper BR1 is closed NVRMO is set to memory writeable.

BR1

1	NVMRO
2	GND

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