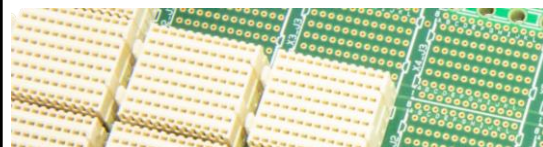


**3U CompactPCI Serial
BACKPLANE without RIO**
2 Slot System Slot Left

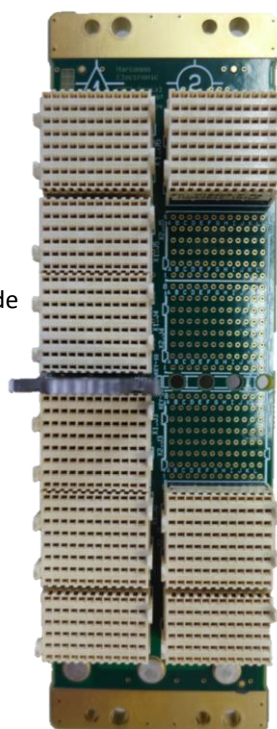
**PICMG CPCI-S.0
R2.0 (2015)**



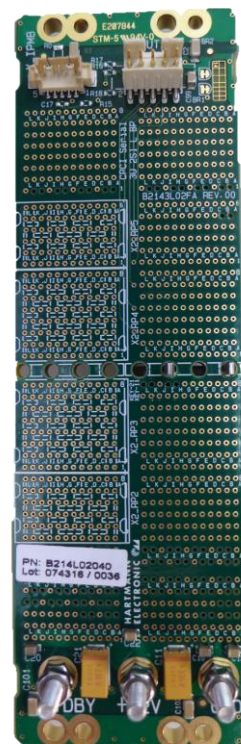
Key Features:

- Compliant to PICMG CPCI-S.0 R2.0 baseline specification
 - 2 Slots CompactPCI Serial without Rear IO
 - Single-Star topology for:
Ethernet, PCI Express, SATA and USB
 - 1 x CPCI S.0 System Slot left (CPU)
 - 1 x CPCI S.0 Peripheral Slot
 - M3 studs for power entry
 - Max. Input current per backplane
12V = 14A, 5V Standby = 2A
 - PCB size 128.50mm x 39.94mm x 4.0 mm
 - 4 HP from slot to slot (20.32 mm)
 - High speed connections for
USB, SATA, PCIe (Gen. 3), Ethernet
 - Guiding element in system slot
 - System Management Bus on the backplane (IPMB)
 - with geographical addressing
 - System Reset Signal
 - Wake Signal
 - Operating temperature: -40° - +85°C
 - Storage temperature: -55°C - +85°C
 - Flammability rating: UL94-V0
 - Custom assembly or modification on request
- **Order number: B214L02040**

Front side

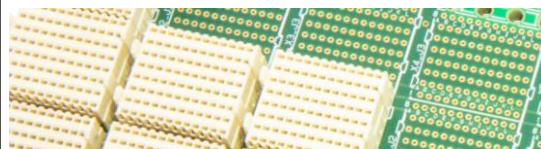


Back side

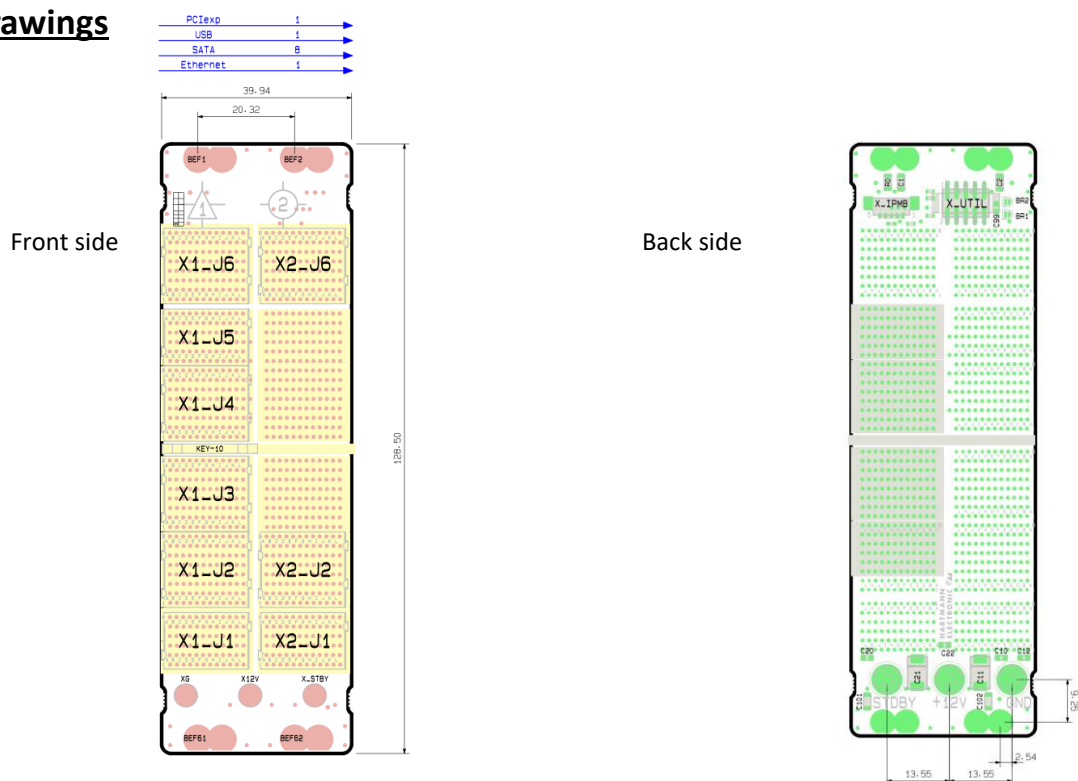


**3U CompactPCI Serial
BACKPLANE without RIO
2 Slot System Slot Left**

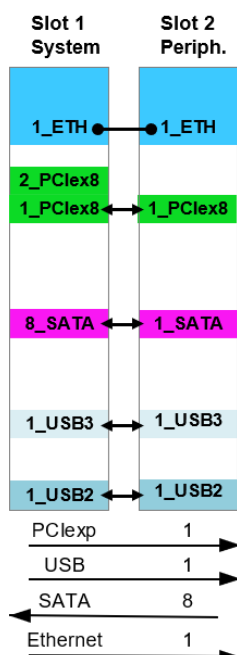
**PICMG CPCI-S.0
R2.0 (2015)**

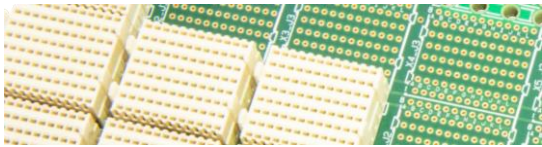


1) Drawings



2) Backplane Topology



3U CompactPCI Serial BACKPLANE without RIO 2 Slot System Slot Left	PICMG CPCI-S.0 R2.0 (2015)	
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3) Pin Assignment IPMB and UTILITY Connector



X_IPMB Pin Assignment

Pin	Signal
1	IPMB_SCL
2	GND
3	IPMB_SDA
4	IPMB_PWR
5	nc

Mating with: Molex 51021-0500



X_UTIL Pin Assignment

Pin	Signal	Pin	Signal
1	GND	2	STDBY
3	+12V	4	PS_ON
5	PWRBTN#	6	PWR_FAIL#
7	WAKE_IO	8	PRST#
9	GND_SENSE	10	+12V SENSE

Can be connected to Power Backplane (e.g. Hartmann 1 slot P-BP part number B210L001A1) with Hartmann utility cable (part number F006.04710, length 350mm)

4) Power Connections M3 screws

The 12V main operating voltages, 5V Standby and GND are supplied with M3 studs.

Max. Input current per backplane

12V = 14A, 5V Standby = 2A



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